

Preliminary_SF3060PT

Super Fast Rectifiers

Features

• Plastic package has Underwriters Laboratory

Flammability Classification 94V-0

- Dual rectifier construction, positive center-tap
- · Planar chip construction
- Low forward voltage, high current high current capability
- · Low thermal resistance, low power loss
- High temperature soldering guaranteed:250 °C,

0.1"(4.06mm) from case for 10 seconds

· RoHS compliant package

Mechanical Data

- · Molding compound meets UL 94 V-0 flammability
- · RoHS compliant, and commercial grade
- Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

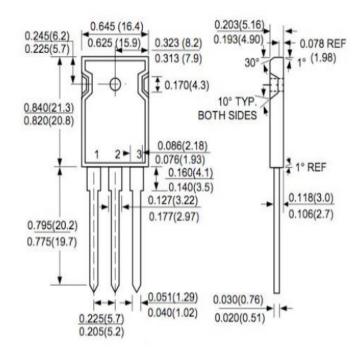
· Polarity: As marked

Package type: TO-247AD

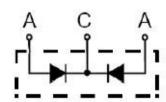
Packing & Order Information

50/Tube; 1,000/Box





Graphic symbol



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Maximum Ratings (Tc=25°C unless otherwise noted)			
Parameter	Symbol	SF3060PT	Unit
Maximum repetitive peak reverse voltage	VRRM	600	V
Working peak reverse voltage	VRWM	420	V
Maximum DC blocking voltage	VDC	600	V
Maximum average forward rectified current TA=100°C	IF(AV)	30	A
Peak forward surge current			
8.3ms single half sine-wave superimposed	IFSM	300	A
on rated load (JEDEC Method)			
Junction Capacitance	Cj	145	pF
Operating junction temperature range	TJ	-55 to +150	°C
Storage temperature range	TSTG	-55 to +175	°C



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Electrical characteristics (Tc=25°C unless otherwise noted)					
Parameter	Symbol	Value		Unit	
1 drameter	Symbol	Typical	Max	Ont	
Instantaneous forward voltage per diode	VF	2.3	2.5	V	
at IF=15A, TA=25°C					
Maximum reverse current per leg Tj=25°C	IR	10		uA	
at working peak reverse voltage Tj=125°C	IK	500		uA	
Reverse Recovery Time	Trr	35		ns	
IF=0.5A,IR=1A, Irr=0.25A					

Thermal characteristics (Tc=25°C unless otherwise noted)					
Parameter	Symbol	Value	Unit		
Typical thermal resistance	Rthja	1.2	°C/W		

Notes:

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

(3) Cj Measured at 1.0MHz and reverse voltage of 4.0V DC.



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