

## Preliminary\_SBLF3060C

### Low VF Schottky Barrier Rectifier

#### **Features**

- · Low forward voltage drop, low power losses
- · High efficiency operation
- Solder bath temperature 275 °C max. 10 s, per JESD

#### 22-B106

· RoHS compliant package

#### **Mechanical Data**

· Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability

· RoHS compliant, and commercial grade

· Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

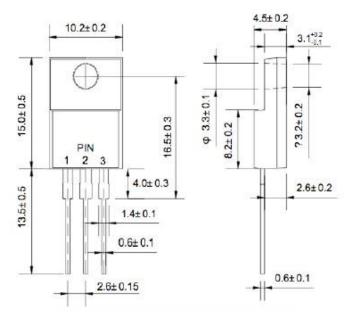
· Polarity: As marked

#### **Packing & Order Information**

50/Tube; 1,000/Box

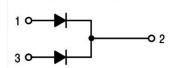


RoHS COMPLIANT



#### **Graphic symbol**

#### **PIN CONNECTIONS**



#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Maximum Ratings (Tc=25°C unless otherwise noted)				
Parameter	Symbol	SBLF3060C	Unit	
Maximum repetitive peak reverse voltage	VRRM	60	V	
Working peak reverse voltage	VRWM	60	V	
Maximum DC blocking voltage	VDC	60	V	
Maximum average forward rectified current per device	IF(AV)	30	A	
per diode	IF(AV)	15	A	
Peak forward surge current				
8.3ms single half sine-wave superimposed	IFSM	180	Α	
on rated load (JEDEC Method)				
Voltage rate of change (rated VR)	dv/dt	10000	V/µs	
Operating junction temperature range	TJ	-55 to +150	°C	
Storage temperature range	TSTG	-55 to +150	°C	



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Electrical characteristics (Tc=25°C unless otherwise noted)					
Parameter	Symbol	Value		Unit	
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Instantaneous forward voltage at IF=15A, Tj=25°C	VF	0.63	0.70	V	
at IF=15A, Tj=125°C		0.60	0.65		
Maximum reverse current per leg Tj=25°C	- IR	0.5		m'A	
at working peak reverse voltage Tj=125°C	IK	45		m'A	

Thermal characteristics (Tc=25°C unless otherwise noted)					
Parameter	Symbol	Value	Unit		
Typical thermal resistance	Rthjc	4.8	°C/W		

#### Notes:

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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