

N-Channel 100-V (D-S) MOSFET

Features

- · Low rDS(on) trench technology
- · Low thermal impedance
- · Fast switching speed
- · RoHS compliant package

Applications:

- PoE Power Sourcing Equipment
- PoE Powered Devices
- Telecom DC/DC converters
- White LED boost converters

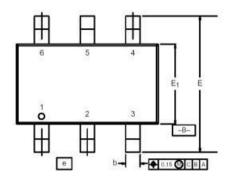
Package type: TOSP-6

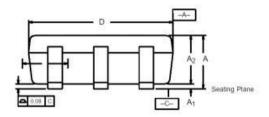
Packing & Order Information

3,000/Reel

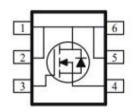








Graphic symbol



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

THE VALUE OF THE THE PERSON OF					
Absolute Maximum Ratings (T _A =25°C Unless Otherwise Noted)					
Symbol	Parameter	Value	Unit		
V_{DS}	Drain-Source Voltage	100	V		
V_{GS}	Gate-Source Voltage	±20	V		
I_{D}	Drain Current -Continuous a (T _A =25°C)	2.2	A		
	Drain Current -Continuous a (TA = 70°C)	1.8	A		
I_{DM}	Pulsed Drain Current ^b	10	A		
P_D	Total Power Dissipation ^a (T _A =25°C)	2	w		
	Total Power Dissipation ^a (T _A =70°C)	1.3	W		
Is	Continuous Source Current (Diode Conduction) ^a	2.5	A		
T_{J},T_{STG}	Operating and Storage Temperature Range	-55 to +150	°C		



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Thermal Data				
Symbol	Parameter	Max.	Units	
$R_{ heta JC}$	Maximum Junction-to-Case ^a (t<=5 sec)	35	°C/W	
$R_{\theta JA}$	Maximum Junction-to- Ambient ^a (t<=5 sec)	50		

Note:

- 1. Surface Mounted on 1"x1" FR4 Board.
- 2. Pulse width limited by maximum junction temperature.

Static					
Symbol	Test Conditions	Min	Typ.	Max.	Units
V_{SD}	$V_{GS} = 0 V$, $I_S = 1.3 A$		0.77		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1		3.5	V
I _{DSS}	$V_{DS} = 80 \ V$, $V_{GS} = 0 \ V$ $V_{DS} = 80 \ V$, $V_{GS} = 0 \ V$, $T_{j=} 55^{\circ}C$			1 25	uA
I _{GSS}	$V_{GS} = 20 \ V , \ V_{DS} = 0 \ V$			±100	nA
I _{D(ON)}	$V_{DS} = 5 \text{ V}$, $V_{GS} = 10 \text{ V}$	5			A
Rds(on)	$V_{GS} = 10 \text{ V}, I_D = 1.5 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 1.4 \text{ A}$			280 355	mΩ
G _{FS} *1	V _{DS} = 15 V,I _D = 1.5 A		5		S

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
C_{ISS}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		1302		pF
Coss	Output Capacitance			423		pF
Crss	Reverse Transfer Capacitance			171		pF
Q_{g}	Total Gate Charge	$V_{DS} = 50 \text{ V}, I_{D} = 1.5 \text{ A},$ $V_{GS} = 4.5 \text{ V}$		11		nC
$Q_{gs} \\$	Gate-Source Charge			6		nC
$Q_{\rm gd}$	Gate-Drain Charge			4		nC
$t_{d(on)}$	Turn-On Dalay Time	$V_{DD} = 50 \text{ V}$, $I_{D} = 1.5 \text{ A}$, $R_{L} = 33.3 \Omega$, $V_{GEN} = 10 \text{ V}$ $R_{GEN} = 6 \Omega$		10		ns
t _r	Rise Time			5		ns
$t_{ m d(off)}$	Turn-Off Dalay Time			22		ns
tf	Fall Time			4		ns

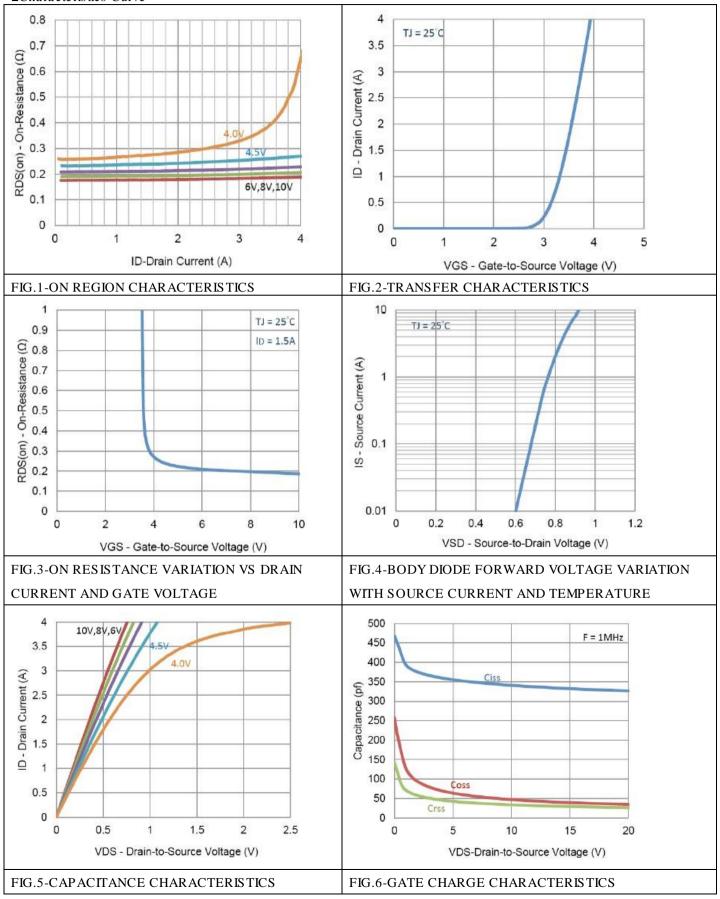
Notes

- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.



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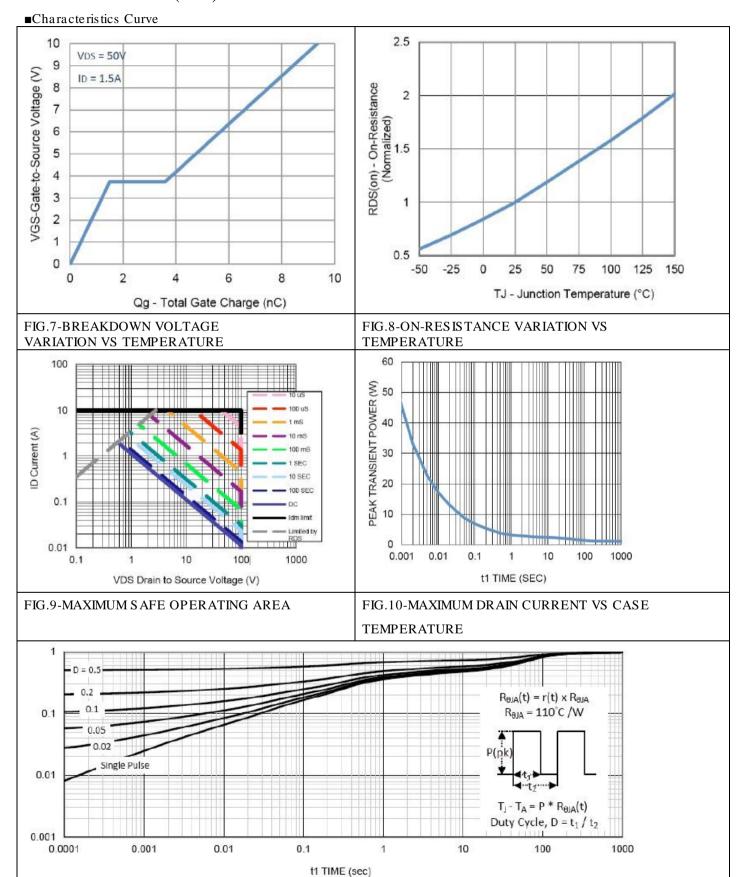


FIG.11-TRANSIENT THERMAL RESPONSE CURVE



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