

#### N-Channel 650V MOSFET

#### **Description**

The MSF10N65 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The ITO-220AB package is universally preferred for all commercial-industrial applications

#### **Features**

- Low On Resistance
- · Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- · RoHS compliant package

#### **Application**

- Power Factor Correction
- LCD TV Power
- Full and Half Bridge Power

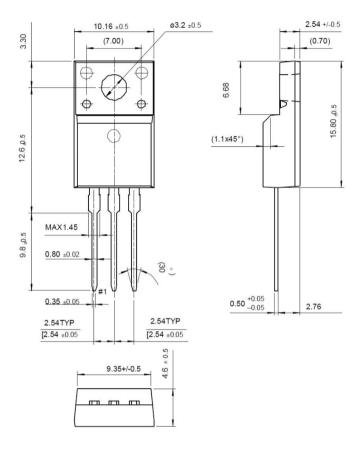
Package type: ITO220-AB

## Packing & Order Information

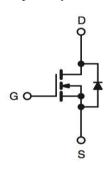
50/Tube; 1,000/Box







#### Graphic symbol



#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)						
Symbol	Parameter	Value	Unit			
$V_{\mathrm{DS}}$	Drain-Source Voltage	650	V			
$V_{GS}$	Gate-Source Voltage	±30	V			
τ_	Drain Current -Continuous (TC=25°C)	10	A			
I <sub>D</sub>	Drain Current -Continuous (TC=100°C)	6.0	A			
$I_{DM}$	Drain Current Pulsed	40	A			
Eas	Single Pulsed Avalanche Energy	710	mJ			
$I_{AR}$	Avalanche Current	10	A			
E <sub>AR</sub>	Repetitive Avalanche Energy	16.2	mJ			
dV/dt	Peak Diode Recovery dV/dt	4.5	V/ns			



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Absolute Maximum Ratings (Tc=25°C unless otherwise noted)						
Symbol Parameter Value Unit						
$P_D$	Power Dissipation (TC = 25 °C)	52	W			
	Power Dissipation (TC=100°C)	0.42	W/°C			
$T_{J},T_{STG}$	Operating and Storage Temperature Range	-55 to +150	°C			

#### NOTE:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=13mH, IAS=10.0A, VDD=50V, RG=25 $\Omega$ , Starting TJ=25 $^{\circ}$ C
- 3. ISD  $\leq$  10.0A, di/dt  $\leq$  200A/ $\mu$ s, VDD  $\leq$  BVDSS, Starting TJ = 25°C
- 4. Pulse test : Pulse Width ≤ 300µs, Duty Cycle ≤ 2%
- 5. Essentially Independent of Operating temperature

Thermal characteristics (Tc=25°C unless otherwise noted)						
Symbol	Parameter	Max.	Units			
Rөлс	Junction-to-Case	2.25	0C/W			
Rөла	Junction-to-Ambient	62.5	°C/W			

On Characteristics							
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units	
$V_{\mathrm{GS}}$	Gate Threshold Voltage	$V_{DS}=V_{GS},I_{D}$ =250 $\mu$ A	3.0		5.0	V	
R <sub>DS</sub> (ON)	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V,I <sub>D</sub> =3A		1.95	2.4	Ω	

Off Chara	Off Characteristics							
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units		
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}$ =0 V , $I_D$ =250 $\mu$ A	900			V		
$\Delta BV_{DSS}$ $/\Delta T_J$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> =250μA, Referenced to 25°C		1.03		V/°C		
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ =900V, $V_{GS}$ = 0 V $V_{DS}$ =720V, $T_{C}$ = 125°C			10 100	μA		
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	$V_{GS}$ =30V, $V_{DS}$ =0V			100	nA		
Igssr	Gate-Body Leakage Current, Reverse	$V_{GS}$ =-30V, $V_{DS}$ =0V			-100	nA		

Dynamic Characteristics							
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units	
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz		1500	2010	pF	
Coss	Output Capacitance			145	190	pF	
C <sub>RSS</sub>	Reverse Transfer Capacitance			15	20	pF	



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Dynamic Characteristics							
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units	
$t_{d(on)}$	Turn-On Time	$V_{DS}$ =450 V, $I_{D}$ =6A, $R_{G}$ =25 $\Omega$		40	80	ns	
$t_{\rm r}$	Turn-On Time			120	240	ns	
$t_{d(off)}$	Turn-Off Delay Time			60	120	ns	
tf	Turn-Off Fall Time			70	140	ns	
$Q_{g}$	Total Gate Charge	V <sub>DS</sub> =720V,I <sub>D</sub> =6A, V <sub>GS</sub> =10 V		33	45	nC	
$Q_{gs}$	Gate-Source Charge			10		nC	
$Q_{\mathrm{gd}}$	Gate-Drain Charge			13		nC	

Source-Drain Diode Maximum Ratings and Characteristics							
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units	
Is	Continuous Source-Drain Diode Forwa	rd Current			6.0		
I <sub>SM</sub>	ISM Pulsed Source-Drain Diode Forward Current				24.0	A	
$V_{SD}$	Source-Drain Diode Forward Voltage	$I_S=6A$ , $V_{GS}=0V$			1.4	V	
$t_{rr}$	Reverse Recovery Time	$I_{S}=6A$ , $V_{GS}=0V$		780		ns	
Qrr	Reverse Recovery Charge	diF/dt=100A/μs		9.0		μC	

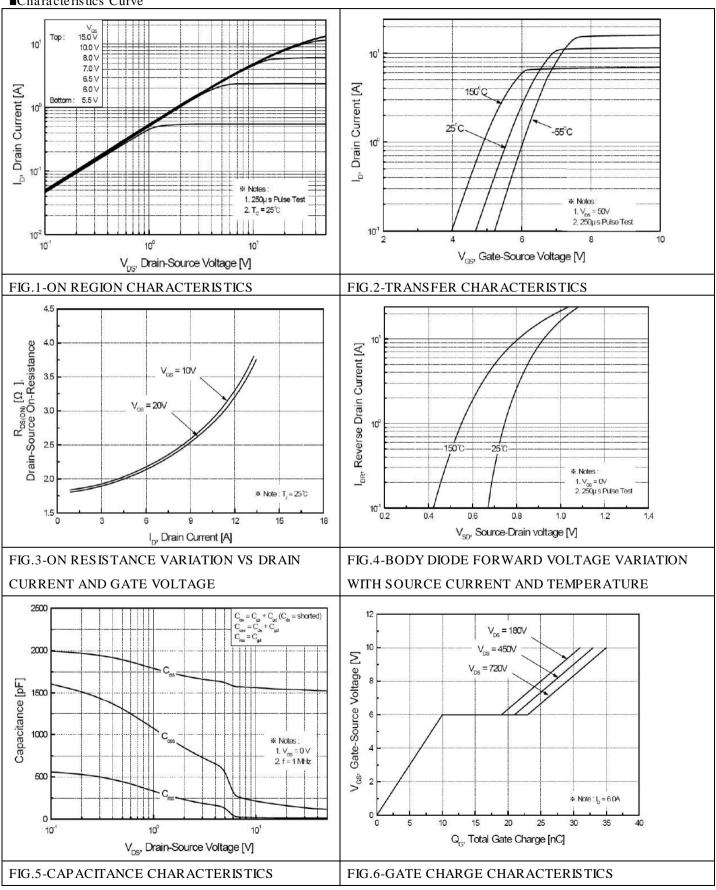
#### Notes;

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=34mH, I\_AS=6A, V\_DD=50V, R\_G=25 $\Omega$ , Starting T\_J=25 $^{\circ}$ C
- 3.  $I_{SD} \leq 6A$ ,  $di/dt \leq 200A/\mu s$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25$ °C
- 4. Pulse Test: Pulse Width  $\leq 300 \,\mu\,\mathrm{s}$ , Duty Cycle  $\leq 2\%$
- 5. Essentially Independent of Operating Temperature



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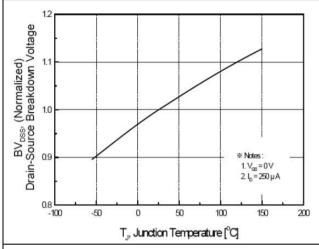
#### ■Characteristics Curve





#### N-Channel 650V MOSFET

#### ■Characteristics Curve



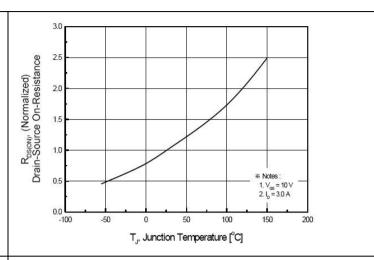


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

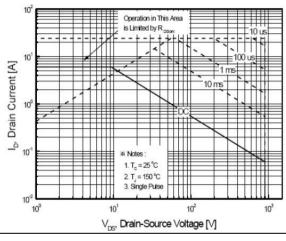


FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

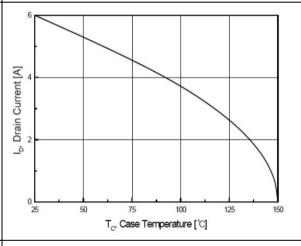


FIG.9-MAXIMUM SAFE OPERATING AREA

FIG.10-MAXIMUM DRAIN CURRENT VS CASE TEMPERATURE

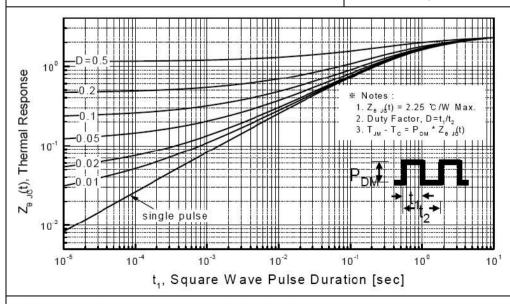


FIG.11-TRANSIENT THERMAL RESPONSE CURVE



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#### ■Characteristics Test Circuit & Waveform

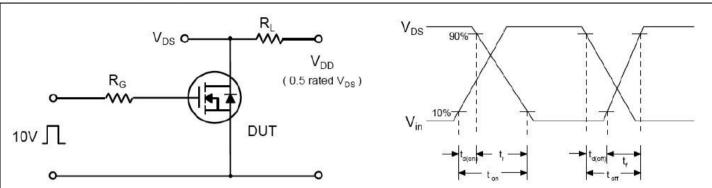


Fig 12. Resistive Switching Test Circuit & Waveforms

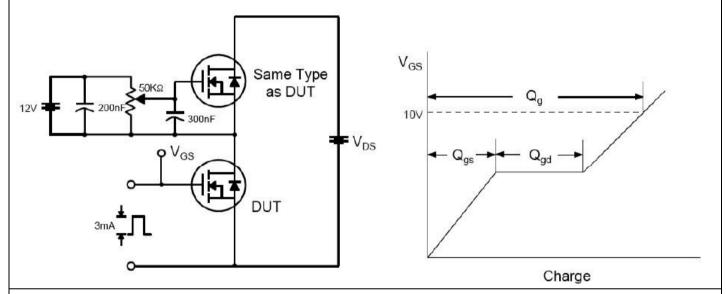


Fig 13. Gate Charge Test Circuit & Waveform

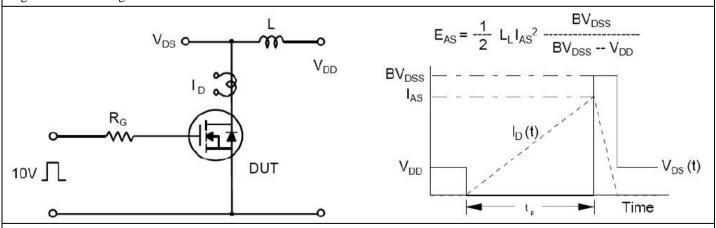


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms



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