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Product Specification

Dual N-Channel Logic Level Enhancement Mode Power MOSFET

MSB22A04Q8

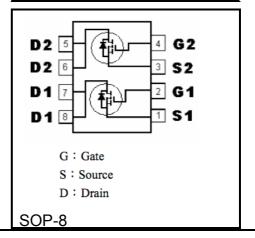
Description

The MSB22A04Q8 provides the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The SOP-8 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

•FEATURES:

- $R_{DS(ON)}=22m\Omega(\partial_{0}V_{GS}=10V, I_{D}=8A$
- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Dual N-ch MOSFET package
- Pb-free lead plating and Halogen-free package

BVDSS: 40VRDS(ON): $22m\Omega$ ID: 8A



Absolute Maximum Ratings (Ta=25°C)

Paramet	Symbol	Limits	Unit		
Drain-Source Voltage	V _{DS}	40	v		
Gate-Source Voltage	V _{GS}	±20			
Continuous Drain Current @ Tc=	ID	8	A		
Continuous Drain Current @ Tc=	10	7			
Pulsed Drain Current		Iдм	32 *1		
Total Power Dissipation	T _A =25°C	D _D	2.4	W	
	T _A =100°C	PD	1.3		
Operating Junction and Storage Temperature Range		Tj, Tstg	-55~+175	°C	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	Rth,j-c	25	°C/W
Thermal Resistance, Junction-to-ambient, max	Rth,j-a	62.5 *2	°C/W

Note: 1. Pulse width limited by maximum junction temperature

2. Surface mounted on 1 in² copper pad of FR-4 board, 125°C/W when mounted on minimum copper pad

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Characteristics (Tc=25°C, unless otherwise specified)

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Symbol	Min.	Тур.	Max.	Unit	Test Conditions	
Static						
BVDSS	40	-	-	V	V _{GS} =0V, I _D =250μA	
V _{GS(th)}	1	1.7	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
I_{GSS}	-	-	±100	nA	$V_{GS}=\pm20$	
T	-	-	1	μА	V _{DS} =32V, V _{GS} =0V	
I _{DSS}	-	-	25		V _{DS} =30V, V _{GS} =0V, Tj=125°C	
I _{D(ON)} *1	8	-	-	Α	$V_{DS} = 5V, V_{GS} = 10V$	
R _{DS(ON)} *1	-	20	22	mΩ	$V_{GS} = 10V, I_D = 8A$	
	-	30	37	mΩ	V _{GS} =4.5V, I _D =5A	
G FS *1	-	20	-	S	V _{DS} =5V, I _D =8A	
Dynamic						
Ciss	-	1205	-			
Coss	-	80	-	pF	$V_{GS}=0V$, $V_{DS}=20V$, $f=1MHz$	
Crss	-	57	-			
Qg *1, 2	-	11	-			
Qgs *1, 2	-	1.8	-	nC	V _{DS} =20V, V _{GS} =10V, I _D =7A	
Qgd *1, 2	-	4.1	-			

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Тур.	Max.	Unit	Test Conditions	
td(ON) *1, 2	-	3.5	-			
tr *1, 2	-	9.5	-		$V_{DS}=20V$, $I_D=1A$, $V_{GS}=10V$,	
td(OFF) *1, 2	_	15	-	ns	R _{GS} =6Ω	
t _f *1, 2	-	6	-			
Source-Drain Diode						
I _S *1	_	-	8			
I _{SM} *3	-	-	24	A		
V _{SD} *1	-	-	1.3	V	I _F =I _S , V _{GS} =0V	

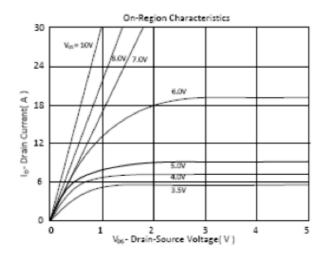
Note: *1.Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%

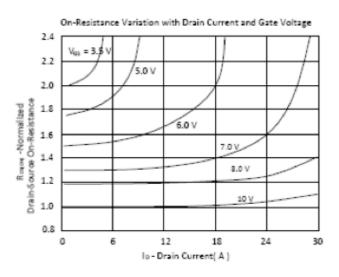
^{*2.}Independent of operating temperature

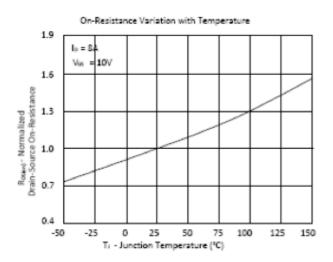
^{*3.}Pulse width limited by maximum junction temperature.

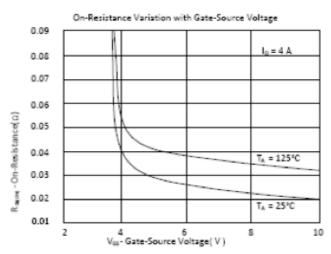
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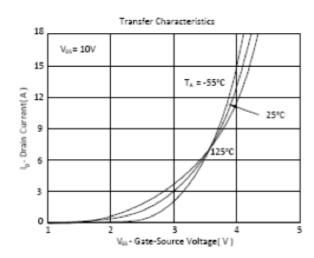
Typical Characteristics

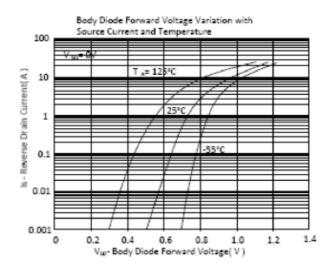












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Typical Characteristics

