

Dual N Channel 60-V (D-S) MOSFET

RoHS

COMPLIANT

Features

- Low rDS(on) trench technology
- Low thermal impedance
- Fast switching speed
- RoHS compliant package

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

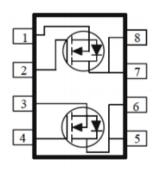
Package type : SO-8

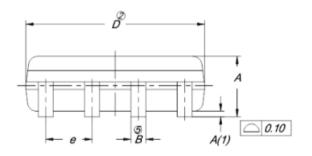
Packing & Order Information

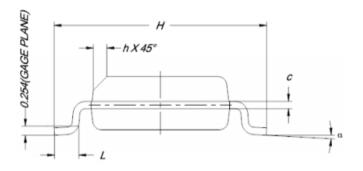
3,000/Reel



Graphic symbol







	MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	
А	1.35	1.55	1.75	
A(1)	0.10	0.18	0.25	
В	0.38	0.45	0.51	
С	0.19	0.22	0.25	
D	4.80	4.90	5.00	
E	3.80	3.90	4.00	
е	1.27 BSC			
н	5.80	6.00	6.20	
L	0.50	0.72	0.93	
α	0°	4°	8°	
h	0.25	0.38	0.50	



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (T _A =25°C unless otherwise specified)					
Symbol	Parameter	Value	Unit		
V _{DS}	Drain-Source Voltage	60	V		
V _{GS}	Gate-Source Voltage	±20	V		
ID	Continuous Drain Current ^a (T _A =25°C)	3.6	А		
	Continuous Drain Current ^a (T _A =70°C)	3.1	А		
I _{DM}	Pulsed Drain Current ^b	20	А		
Is	Continuous Source Current (Diode Conduction) ^a	1.7	А		
P _D	Power Dissipation ^a ($T_A = 25^{\circ}C$)	2.1	W		
	Power Dissipation ^a ($T_A = 70^{\circ}C$)	1.3	W		
T_J/T_{STG}	Operating Junction and Storage Temperature	1.3	°C		

Thermal Resistance Ratings					
Symbol	Parameter Maximum Un				
$R_{\theta JA}$	Maximum Junction-to-Ambient ^a (t <= 10 sec)	62.5	°C/W		
	Maximum Junction-to-Ambient ^a (Steady-State)	110	C/ W		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

Static						
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
$V_{GS(th)}$	Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
I _{GSS}	Gate-Body Leakage	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			±100	nA
Idss	Zero Gate Voltage Drain Current				1 25	uA
I _{D(on)}	On-State Drain Current	$V_{DS} = 5 V, V_{Gs} = 10 V$	8			A
^r DS (on)	Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 2.9 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 2.7 \text{ A}$			89 104	mΩ
g _{fs}	Forward Tranconductance	$V_{GS} = 15 \text{ V}, I_D = 2.9 \text{ A}$		10		S
Vsd	Diode Forward Voltage	$I_S = 0.9 \ A \ , \ V_{GS} = 0 \ V$		0.78		V



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Dynamic						
Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
Qg	Total Gate Charge	$-V_{DS} = 30 \text{ V}, \text{ I}_{D} = 2.9 \text{ A},$ $-V_{GS} = 4.5 \text{ V}$		40		nC
Q_{gs}	Gate-Source Charge			1.2		nC
\mathbf{Q}_{gd}	Gate-Drain Charge			2.1		nC
t _{d(on)}	Turn-On Delay Time	$I_{D} = 2.9 \text{ A}, R_{L} = 10.4 \Omega,$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$ $V_{DS} = 30 \text{ V}$		3		ns
tr	Rise Time			6		ns
td(off)	Turn-Off Delay Time			17		ns
tf	Fall Time			5		ns
CISS	Input Capacitance	$V_{DS} = 15 V$ - f = 1 MHz , $V_{GS} = 0 V$		297		pF
Coss	Output Capacitance			40		pF
Crss	Reverse Transfer Capacitance			28		pF

Notes

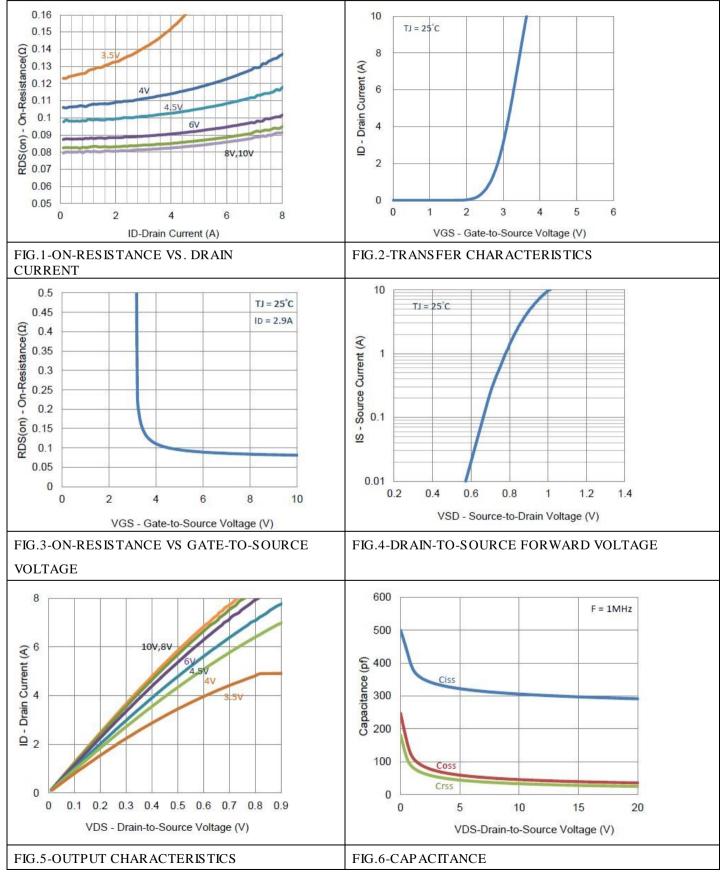
a. Pulse test: PW <= 300us duty cycle <= 2%.

b. Guaranteed by design, not subject to production testing.



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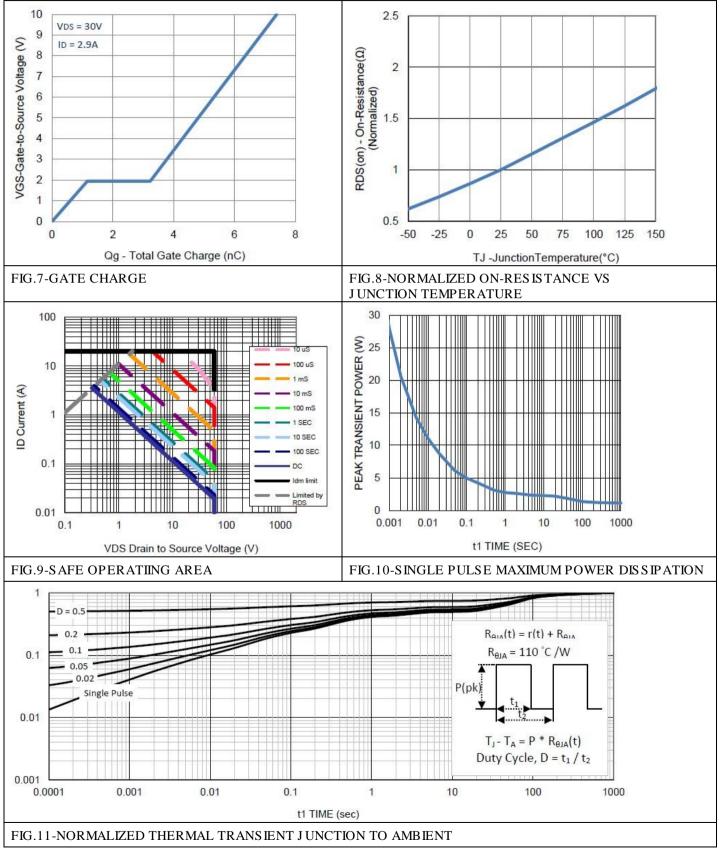
■Typical Electrical Characteristics





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Typical Electrical Characteristics





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