

# N-Channel 60-V (D-S) MOSFET

#### **Features**

- Low rDS(on) trench technology
- Low thermal impedance
- Fast switching speed
- Low thermal impedance copper lead frame DFN5X6-8L saves board space
- RoHS compliant package

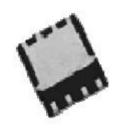
#### **Typical Applications:**

- DC/DC Conversion Circuits
- Motor Drives

Package type: DFN5X6-8L

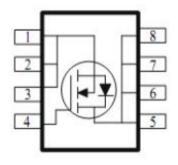
#### **Packing & Order Information**

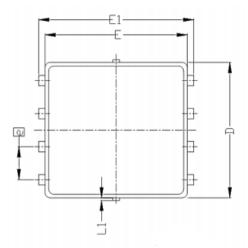
3,000/Reel

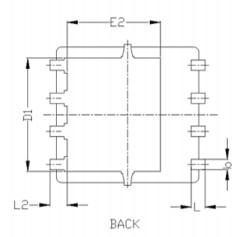


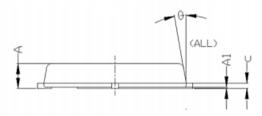
RoHS COMPLIANT

Graphic symbol









SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES				
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX		
Α	0.85	0.95	1.00	0.033	0.037	0. 039		
Al	0.00	_	0.05	0.000		0.002		
b	0.30	0.40	0.50	0.012	0.016	0.020		
С	0.15	0.20	0.25	0.006	0.008	0.010		
D	5, 20 BSC			0. 205 BSC				
D1	4. 35 BSC			0. 171 BSC				
E E1	5, 55 BSC			0. 219 BSC				
E1	6. 05 BSC			0. 238 BSC				
E2	3. 62 BSC			0. 143 BSC				
e	1. 27 BSC			0. 050 BSC				
L	0.45	0.55	0.65	0.018	0.022	0.026		
L1	0		0.15	0		0.006		
L2	0.68 REF			0. 027 REF				
θ	0°		10°	0°		10°		



# N-Channel 60-V (D-S) MOSFET

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

WE HAVE WITH THE SELECT THE SELEC						
Absolute Maximum Ratings (T <sub>A</sub> =25°C unless otherwise specified)						
Symbol	Parameter	Value	Unit			
$V_{DS}$	Drain-Source Voltage	60	V			
$V_{GS}$	Gate-Source Voltage	±20	V			
$I_D$	Continuous Drain Current <sup>a</sup> (T <sub>A</sub> =25°C)	29	A			
	Continuous Drain Current <sup>a</sup> (T <sub>A</sub> =70°C)	23	A			
$I_{DM}$	Pulsed Drain Current <sup>b</sup>	100	A			
Is	Continuous Source Current (Diode Conduction) <sup>a</sup>	7.3	A			
PD	Power Dissipation <sup>a</sup> (T <sub>A</sub> =25°C)	5	W			
	Power Dissipation <sup>a</sup> (T <sub>A</sub> =70°C)	3.2	W			
T <sub>J</sub> /T <sub>STG</sub>	Operating Junction and Storage Temperature	-55 to +150	°C			

Thermal Resistance Ratings					
Symbol	Parameter Maximum Units				
RөлА	Maximum Junction-to-Ambient <sup>a</sup> (t <= 10 sec)	25	°C/W		
	Maximum Junction-to-Ambient <sup>a</sup> (Steady-State)	65	C/ <b>VV</b>		

#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Static						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$V_{GS(th)}$	Gate-Source Threshold Voltage	$V_{DS}=V_{GS},I_D\!=\text{-250}\mu\text{A}$	0.4			V
Igss	Gate-Body Leakage	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			±100	nA
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			1 10	uA
I <sub>D(on)</sub>	On-State Drain Current	$V_{DS} = 5 \text{ V}, V_{Gs} = 10 \text{ V}$	40			A
rDS (on)	Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 16 \text{ A}$			4.2 4.7	mΩ
gfs	Forward Tranconductance	$V_{GS} = 15 \text{ V}, I_D = 20 \text{ A}$		22		S
V <sub>SD</sub>	Diode Forward Voltage	$I_S = 3.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.74		V



# N-Channel 60-V (D-S) MOSFET

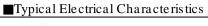
Dynamic						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$Q_{\rm g}$	Total Gate Charge	$V_{DS} = 30 \text{ V}, I_{D} = 4.5 \text{ A},$ $V_{GS} = 20 \text{ V}$		48		nC
$Q_{gs}$	Gate-Source Charge			18		nC
$Q_{\mathrm{gd}}$	Gate-Drain Charge			20		nC
t <sub>d(on)</sub>	Turn-On Delay Time	$I_{D} = 20 \text{ A}, R_{L} = 1.5 \Omega,$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$ $V_{DS} = 30 \text{ V}$		16		ns
$t_{\rm r}$	Rise Time			17		ns
$t_{ m d(off)}$	Turn-Off Delay Time			136		ns
tf	Fall Time			36		ns
C <sub>ISS</sub>	Input Capacitance	$V_{DS} = 15 \text{ V}$ - f = 1 MHz , $V_{GS} = 0 \text{ V}$		14642		pF
Coss	Output Capacitance			433		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			427		pF

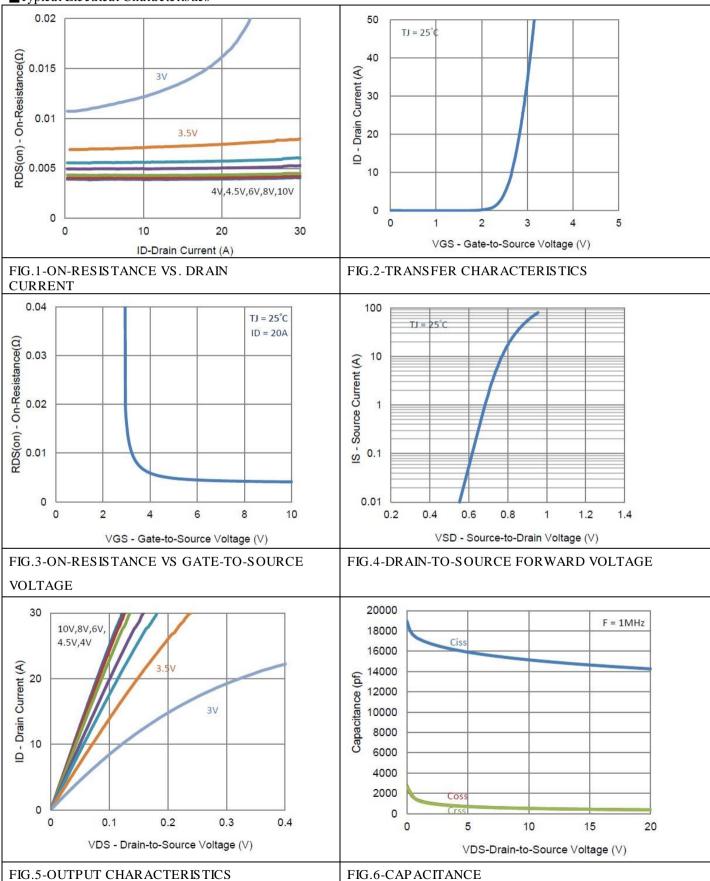
#### Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.



## N-Channel 60-V (D-S) MOSFET

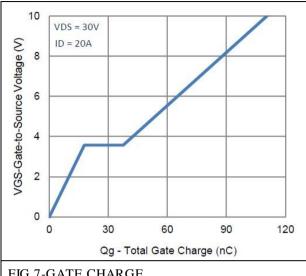






## N-Channel 60-V (D-S) MOSFET

#### ■Typical Electrical Characteristics



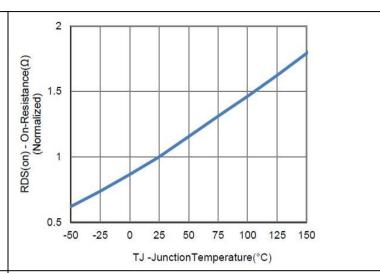


FIG.7-GATE CHARGE

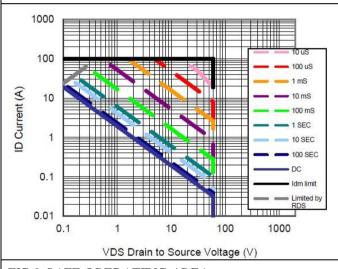


FIG.8-NORMALIZED ON-RESISTANCE VS JUNCTION TEMPERATURE

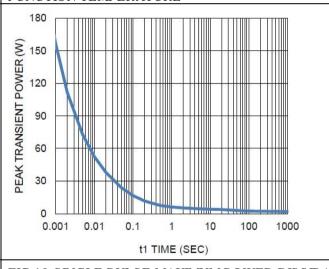


FIG.9-SAFE OPERATIING AREA



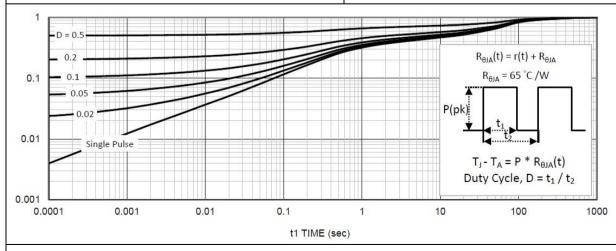


FIG.11-NORMALIZED THERMAL TRANSIENT JUNCTION TO AMBIENT



N-Channel 60-V (D-S) MOSFET

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Bruckewell Technology Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Bruckewell"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product. Bruckewell makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Bruckewell disclaims

- (i) Any and all liability arising out of the application or use of any product.
- (ii) Any and all liability, including without limitation special, consequential or incidental damages.
- (iii) Any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Bruckewell's knowledge of typical requirements that are often placed on Bruckewell products in generic applications.

Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time.

Product specifications do not expand or otherwise modify Bruckewell's terms and conditions of purchase, including but not limited to the warranty expressed therein.