

MS30N06X

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BV _{DSS}	30V
R _{DS(ON)}	4.5mΩ
I _D	40A

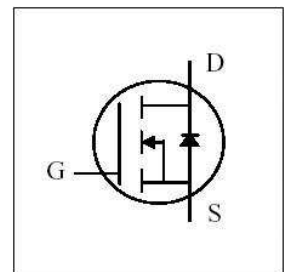
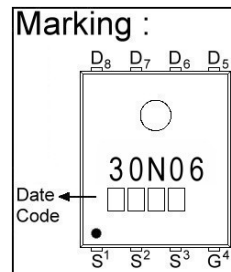
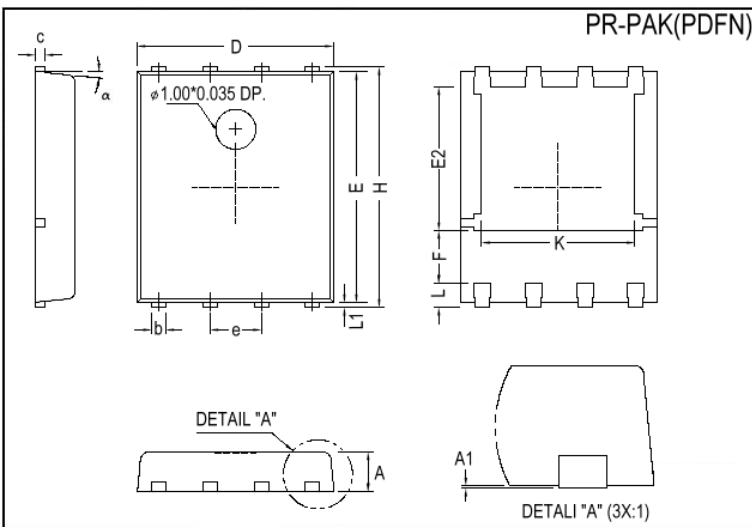
Description

The MS30N06X uses advanced Trench technology and designs to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications. The GPR30N06 meet the RoHS and Green Product requirement, 100% EAS and R_g guaranteed with full function reliability approved.

Features

- Low On-Resistance
- Low Input Capacitance
- Green Device Available
- Low Miller Charge
- 100% EAS and R_g Guaranteed

Package Dimensions



REF.	Millimeter			REF.	Millimeter		
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.85	1.00	1.15	E	5.70	-	5.90
A1	0.00	-	0.10	e	-	1.27	-
b	0.30	-	0.51	H	5.90	-	6.20
c	0.20	-	0.30	L	-	0.60	-
D	4.80	-	5.00	L1	0.06	-	0.20
F	1.10REF.			α	0°	-	12°
E2	3.50REF.			K	3.70	3.90	4.10

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D @T _C =25°C	40	A
	I _D @T _C =70°C	40	A
Pulsed Drain Current ¹	I _{DM}	100	A
Continuous Drain Current	I _D @T _A =25°C	31	A
	I _D @T _A =70°C	25	A
Total Power Dissipation	P _D @T _C =25°C	36	W
	P _D @T _A =25°C	4.2	W
Single Pulse Avalanche Energy, L=0.1mH	E _{AS}	72	mJ
Single Pulse Avalanche Current, L=0.1mH	I _{AS}	38	A
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Conditions	Max. Value	Unit
Thermal Resistance Junction-ambient ²	R _{θJA}	Steady State	30	°C/W
Thermal Resistance Junction-case ²	R _{θJC}	Steady State	3.5	°C/W

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Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$
Gate Threshold Voltage	$V_{GS(th)}$	1.15	-	2.2	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
Forward Transconductance ¹	g_{fs}	-	82	-	S	$V_{DS}=15\text{V}, I_D=19\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS} = \pm 20\text{V}$
Drain-Source Leakage Current	I_{DSS}	-	-	1	μA	$V_{DS}=30\text{V}, V_{GS}=0$
Static Drain-Source On-Resistance ¹	$R_{DS(ON)}$	-	3.3	4.5	m Ω	$V_{GS}=10\text{V}, I_D=19\text{A}$
		-	4.6	5.6		$V_{GS}=4.5\text{V}, I_D=16\text{A}$
Total Gate Charge ¹	Q_g	-	12	-	nC	$I_D=19\text{A}$ $V_{DS}=15\text{V}$ $V_{GS}=4.5\text{V}$
Gate-Source Charge	Q_{gs}	-	6	-		
Gate-Drain ("Miller") Change	Q_{gd}	-	5	-		
Turn-on Delay Time ¹	$T_{d(on)}$	-	24	-	ns	$V_{DS}=15\text{V}$ $I_D=10\text{A}$ $V_{GS}=4.5\text{V}$ $R_G=1.0\Omega$ $R_L=1.5\Omega$
Rise Time	T_r	-	21	-		
Turn-off Delay Time	$T_{d(off)}$	-	25	-		
Fall Time	T_f	-	17	-		
Input Capacitance	C_{iss}	-	1750	-	pF	$V_{GS}=0\text{V}$ $V_{DS}=15\text{V}$ $f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	360	-		
Reverse Transfer Capacitance	C_{rss}	-	150	-		
Gate Resistance	R_g	-	3.2	5.0	Ω	$f=1.0\text{MHz}$

Guaranteed Avalanche Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Single Pulse Avalanche Energy ³	EAS	48	-	-	mJ	$V_{DD}=20\text{V}, L=0.1\text{mH}, I_{AS}=31\text{A}$

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Diode Forward Voltage ¹	V_{SD}	-	0.8	1.2	V	$I_S=10\text{A}, V_{GS}=0\text{V}$
Continuous Source Current ¹	I_S	-	-	40	A	---
Reverse Recovery Time	t_{rr}	-	25	-	ns	$I_F=10\text{A}, di/dt=100\text{A}/\mu\text{s}$
Reverse Recovery Charge	Q_{rr}	-	17	-	nC	$T_J=25^\circ\text{C}$

Notes: 1. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design. $R_{\theta JA}$ shown below for single device operation on FR-4 in still air.

3. The Min. value is 100% EAS tested guarantee.

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Typical Characteristics

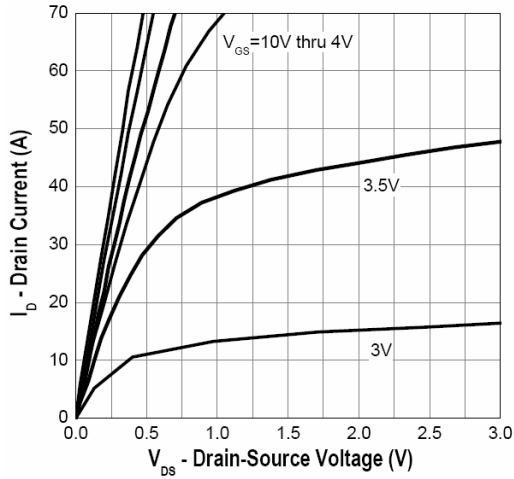


Fig.1 Typical Output Characteristics

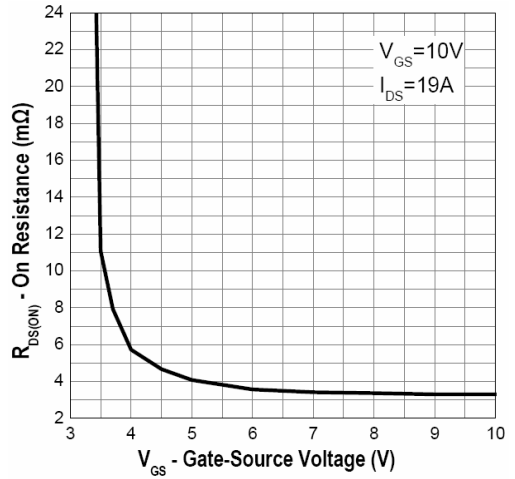


Fig.2 On-Resistance vs. G-S Voltage

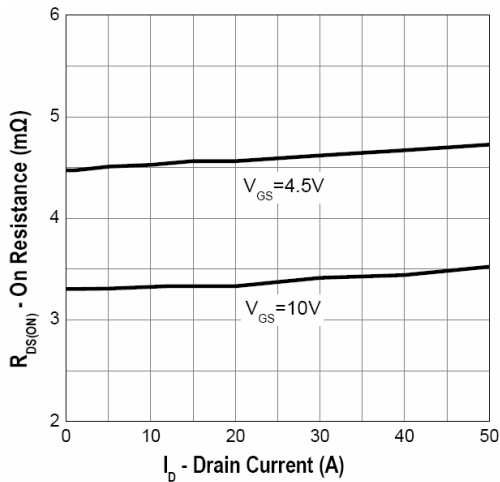


Fig.3 On-Resistance vs. Drain Current

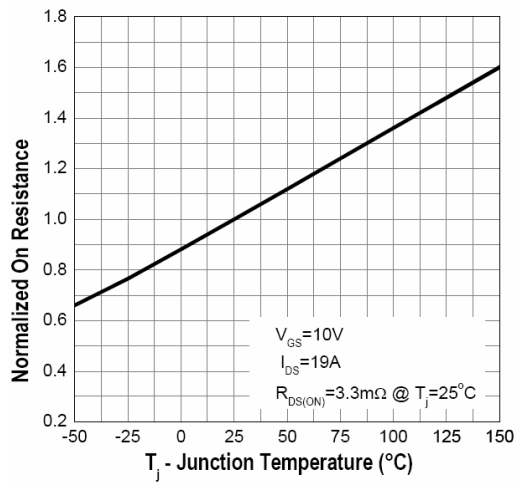


Fig.4 Normalized $R_{DS(on)}$ vs. T_J

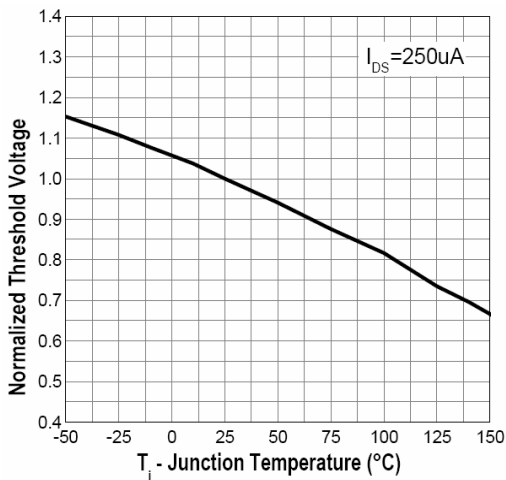


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

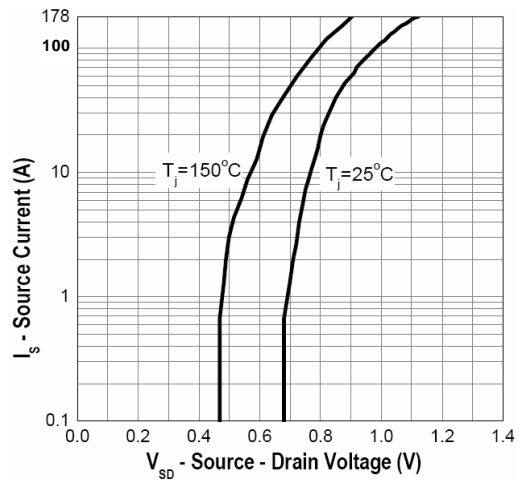


Fig.6 Forward Characteristics of Reverse

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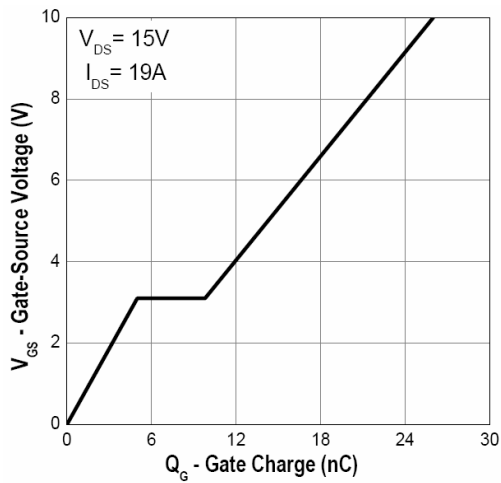


Fig.7 Gate Charge Characteristics

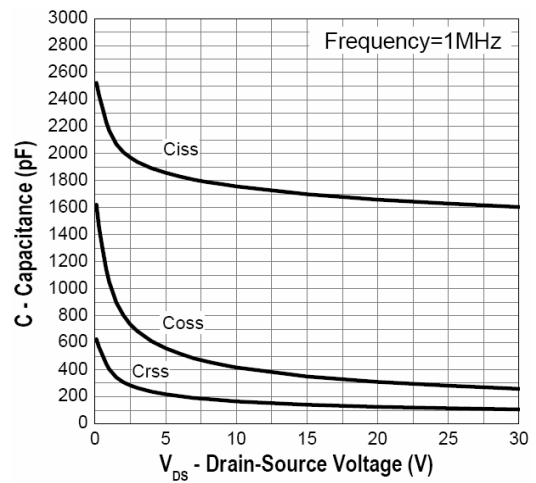


Fig.8 Capacitance Characteristics

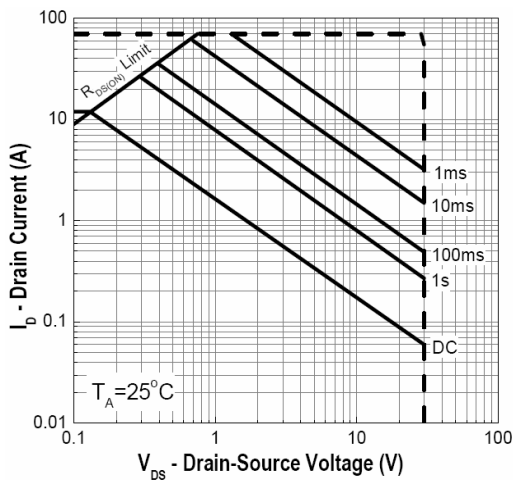


Fig.9 Safe Operating Area

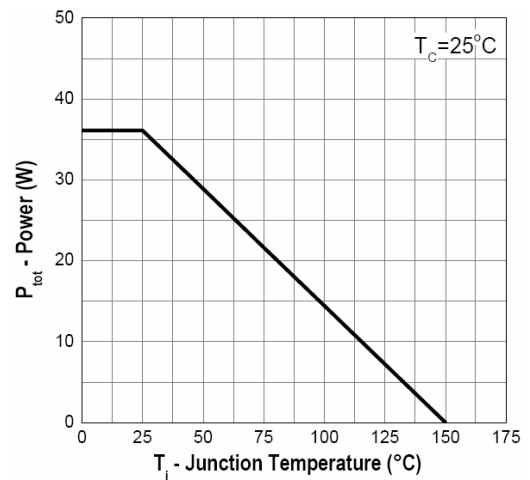


Fig.10 Power Dissipation

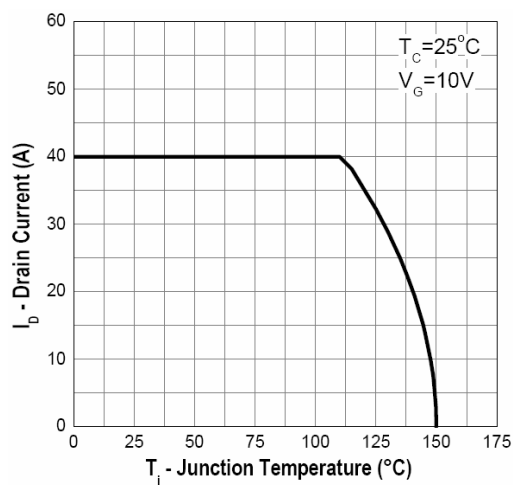


Fig.11 Drain Current vs. T_j

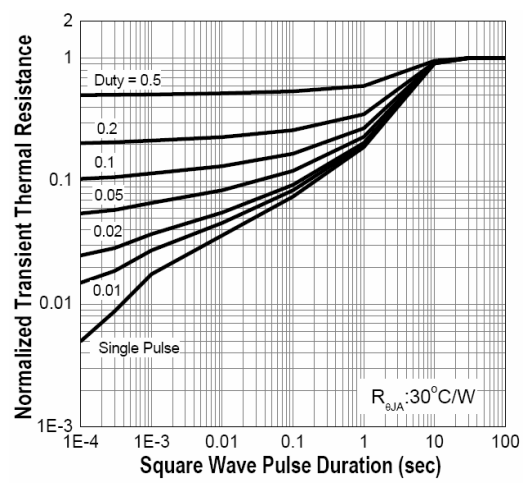


Fig.12 Transient Thermal Impedance