

MS23N70

N-Channel 100-V (D-S) MOSFET

Description

These miniature surface mount MOSFETs utilize High Cell Density process. Low rDS(on) assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are lower voltage application, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

Features

- Low rDS(on) trench technology
- Low thermal impedance
- Fast switching speed
- RoHS compliant package

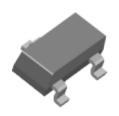
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

Package type: SOT-23

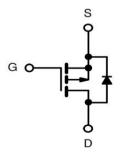
Packing & Order Information

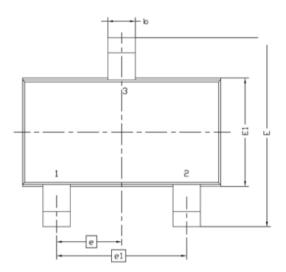
3,000/Reel

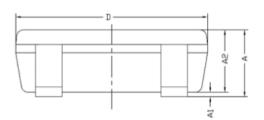


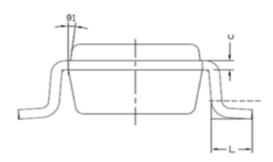
RoHS

Graphic symbol









Symbol	MILLIMETERS			
Symbol	MIN	MAX		
Α	8.0	1.2		
A1	0	0.1		
A2	0.7	1.1		
b	0.3	0.5		
С	0.1	0.2		
D	2.7	3.1		
E	2.6	3		
E1	1.4	1.8		
е	0.95 BSC			
e1	1.9 BSC			
L	0.3	0.6		
θ1	7° NOM			



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (Tc=25°C unless otherwise specified)					
Symbol	Parameter	Value	Unit		
V_{DS}	Drain-Source Voltage	100	V		
V_{GS}	Gate-Source Voltage	±20	V		
I_D	Continuous Drain Current ^a (T _A =25°C)	1.5	A		
	Continuous Drain Current ^a (T _A =70°C)	1.2	A		
I_{DM}	Pulsed Drain Current ^b	10	A		
Is	Continuous Source Current (Diode Conduction) ^a	1.6	A		
P _D	Power Dissipation ^a (T _A =25°C)	1.3	W		
	Power Dissipation ^a (T _A =70°C)	0.8	W		
T _J /T _{STG}	Operating Junction and Storage Temperature	-55 to +150	°C		

Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
$R_{\theta JA}$	Maximum Junction-to-Ambient C/W ^a (t <= 10 sec)	100	°C/W		
	Maximum Junction-to-Ambient C/W ^a (Steady-State)	166	C/W		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical	Characteristics					
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = -250~\mu A$	1			V
I_{GSS}	Gate-Body Leakage	$V_{DS} = 0 V$, $V_{GS} = 20 V$			±100	nA
I _{DS S}	Zero Gate Voltage Drain Current	$\begin{aligned} V_{DS} &= 80 \ V \ , \ V_{GS} &= 0 \ V \\ V_{DS} &= 80 \ V \ , \ V_{GS} &= 0 \ V \ , \ T_{J} &= 55 \ ^{\circ}C \end{aligned}$			1 10	uA
I _{D(on)}	On-State Drain Current ^A	$V_{DS}=5\ V\ ,\ V_{GS}=10\ V$	4			A
rDS (on)	Drain-Source On-Resistance ^A	$V_{DS} = 10 \text{ V}, I_{D} = 1.2 \text{ A}$ $V_{DS} = 4.5 \text{ V}, I_{D} = 1.0 \text{ A}$			280 355	mΩ
gfs	Forward Tranconductance ^A	$V_{GS} = 15 \text{ V}, I_{D} = 1.2 \text{ A}$		5		S
V_{SD}	Diode Forward Voltage	$I_S = 0.8 \text{ A}$, $V_{GS} = 0 \text{ V}$		0.75		V



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Dynamic ^b						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$Q_{\rm g}$	Total Gate Charge	$V_{DS} = 50 \text{ V}, I_{D} = 1.2 \text{ A},$ $V_{GS} = 4.5 \text{ V}$		3.9		nC
Q_{gs}	Gate-Source Charge			1.3		nC
$Q_{\rm gd}$	Gate-Drain Charge			2.0		nC
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 50 \ V \ , \ I_D = 1.2 \ A \ , \\ V_{GEN} = 10 \ V \ , \ R_L = 41.7 \ \Omega \ , \\ R_{GEN} = 6 \ \Omega$		4.8		ns
$t_{\rm r}$	Rise Time			3.9		ns
$t_{ m d(off)}$	Turn-Off Delay Time			12.7		ns
tf	Fall Time			3.2		ns
C _{ISS}	Input Capacitance	$V_{DS} = 15 \ V \ , \ V_{GS} = 0 \ V \ ,$ $f = 1.0 MHz$		332		pF
Coss	Output Capacitance			40		pF
C _{RSS}	Reverse Transfer Capacitance			29		pF
Rg	Gate Resistance	f = 1 MHz		0.3		Ω

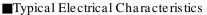
Notes

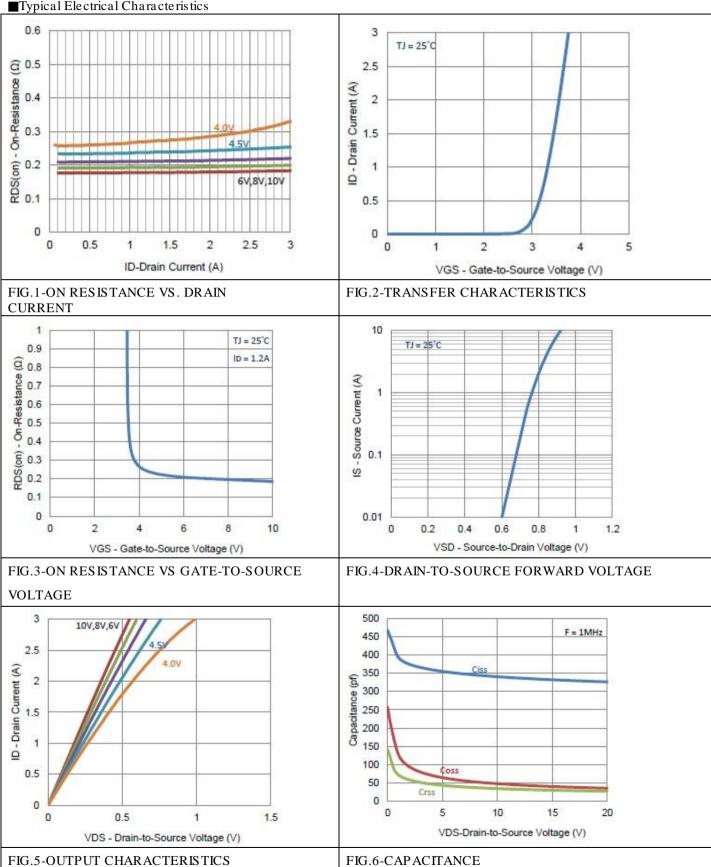
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.



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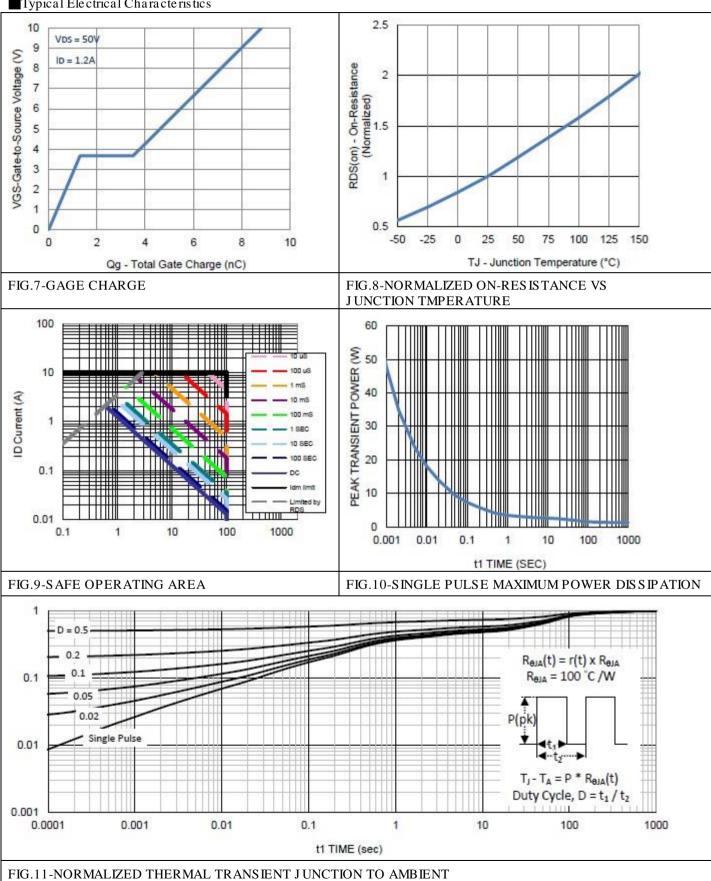




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■Typical Electrical Characteristics





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