

# MS 18N50

## 500V N-channel MOSFET

### Description

The MS18N50 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-220AB package is universally preferred for all commercial-industrial applications

### Features

- Originative New Design
- Very Low Intrinsic Capacitances
- Excellent Switching Characteristics
- 100% EAS Test
- Extended Safe Operating Area
- RoHS compliant package

### Application

- High current, High speed switching
- PFC (Power Factor Correction)
- SMPS (Switched Mode Power Supplies)

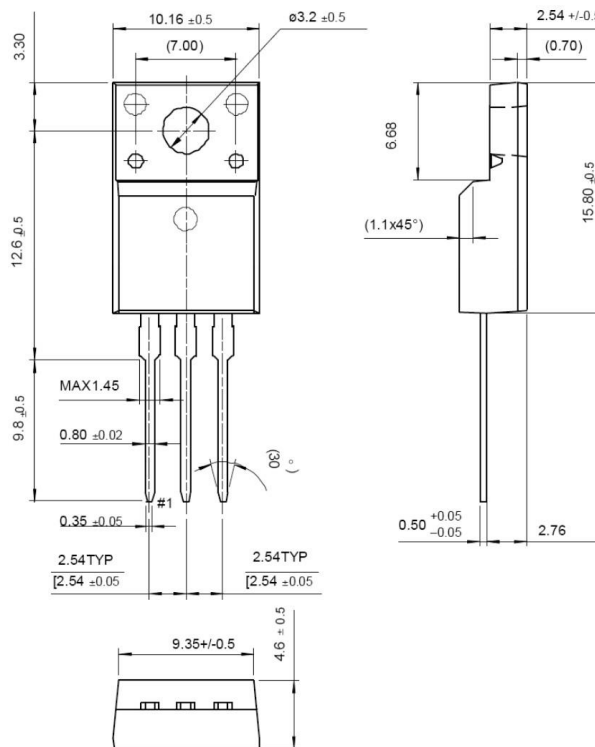
**Package type :** TO-220AB

### Packing & Order Information

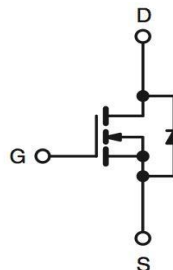
50/Tube ; 1,000/Box



**RoHS  
COMPLIANT**



### Graphic symbol



## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

### Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	500	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub>	Drain Current -Continuous (TC=25°C)	18	A
	Drain Current -Continuous (TC=100°C)	10.8	A
I <sub>DM</sub>	Drain Current -Pulsed	72	A
E <sub>AS</sub>	Single Pulsed Avalanche Energy	990	mJ
E <sub>AR</sub>	Repetitive Avalanche Energy	23.5	mJ
dV/dt	Peak Diode Recovery dV/dt	4.5	V/ns
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature	-55~+150	°C
P <sub>D</sub>	Power Dissipation (TC=25°C)	238	W
	Power Dissipation (TC=100°C)	1.8	W

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- Drain current limited by maximum junction temperature

#### Thermal Characteristics

Symbol	Parameter	Value	Units
Rthjc	Thermal Resistance resistance	0.53	°C/W
RθJA	Thermal Resistance resistance	62.5	

#### Static Characteristics

Symbol	Test Conditions	Min	Typ.	Max.	Units
V <sub>GS</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	3.0		5.0	V
BV <sub>DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250μA	500	--	--	V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	I <sub>D</sub> = 250μA, Referenced to 25°C	--	0.6	--	V/°C
I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V, T <sub>C</sub> = 125°C	--	--	1 10	uA
I <sub>GSSF</sub>	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V	--	--	100	nA
I <sub>GSSR</sub>	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V			-100	nA
*R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 9 A	--	0.25	0.32	Ω

#### Dynamic Characteristics

Symbol	Test Conditions	Min	Typ.	Max.	Units
C <sub>ISS</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f=1.0MHz	--	2500	--	pF
C <sub>OSS</sub>		--	400	--	pF
C <sub>RSS</sub>		--	40	--	pF
t <sub>d(on)</sub>	V <sub>DD</sub> = 250 V, I <sub>D</sub> = 18 A, R <sub>G</sub> = 25 Ω	--	70	--	ns
t <sub>r</sub>		--	190	--	ns
t <sub>d(off)</sub>		--	100	--	ns
t <sub>f</sub>		--	100	--	ns
Q <sub>g</sub>	V <sub>DD</sub> = 400 V, I <sub>D</sub> = 18 A, V <sub>GS</sub> = 10 V	--	48.5	--	nC
Q <sub>gs</sub>		--	14	--	nC
Q <sub>gd</sub>		--	22	--	nC

#### Source-Drain Diode Characteristics

Symbol	Test Conditions	Min	Typ.	Max.	Units
I <sub>S</sub>		--	--	18	A
I <sub>SM</sub>		--	--	72	
V <sub>SD</sub>	I <sub>S</sub> = 18 A, V <sub>GS</sub> = 0 V	--	--	1.5	V
t <sub>rr</sub>	I <sub>F</sub> = 18 A, V <sub>GS</sub> = 0 V	--	550	--	ns
Q <sub>rr</sub>	diF/dt=100A/us	--	5.5	--	nC

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Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $L= 5.5\text{mH}$ ,  $I_{AS}= 18.0\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
3.  $I_{SD} \leq 16.0\text{ A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq \text{BVDSS}$ , Starting  $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$
5. Essentially Independent of Operating Temperature

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#### ■ Characteristic Curves

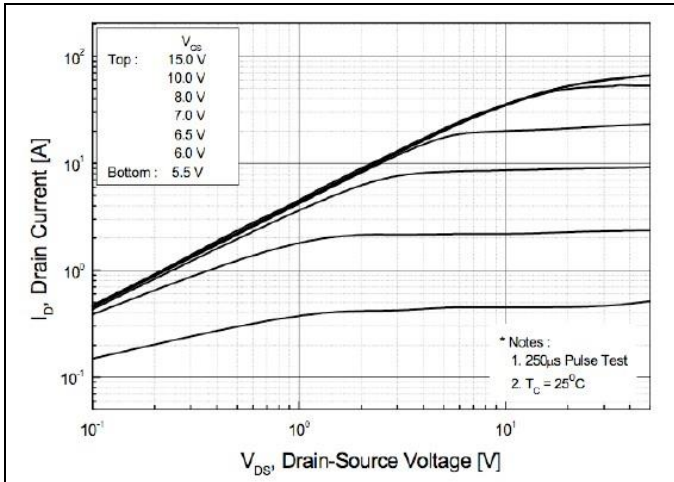


FIG. 1 - ON REGION CHARACTERISTICS

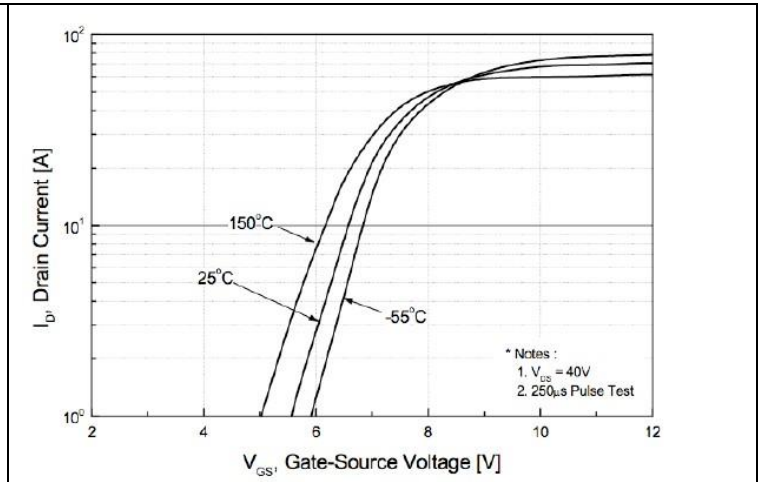


FIG. 2 - TRANSFER CHARACTERISTICS

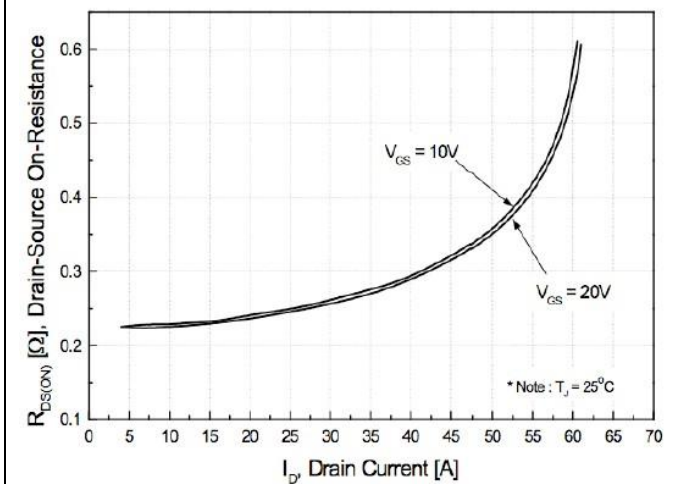


FIG. 3 - ON RESISTANCE VARIATION VS DRAIN CURRENT AND GATE VOLTAGE

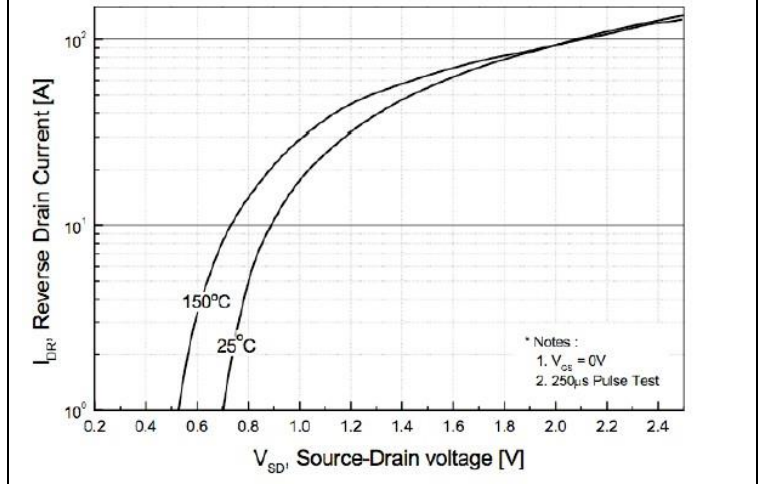


FIG. 4 - BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE

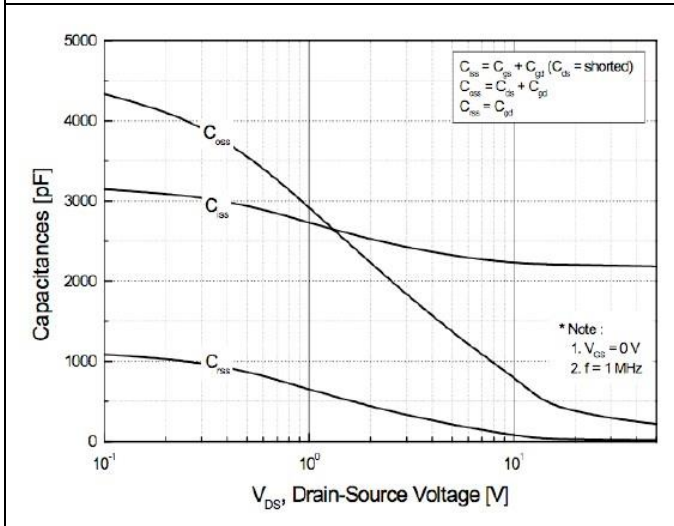


FIG. 5 - CAPACITANCE CHARACTERISTICS

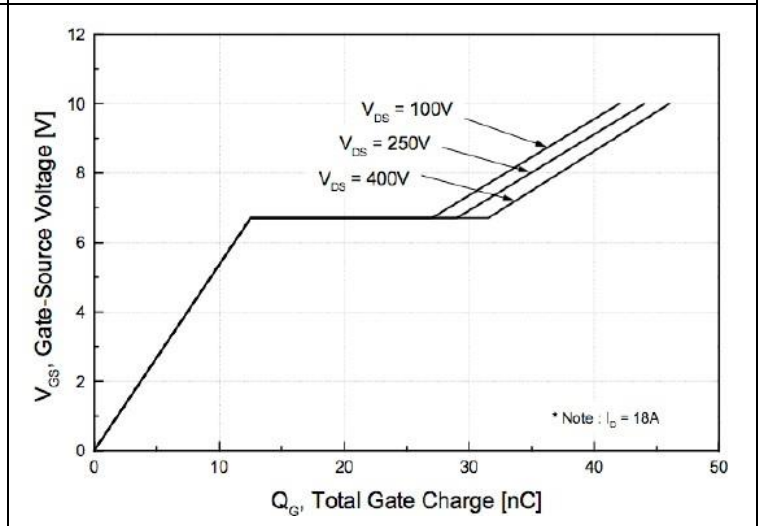


FIG. 6 - GATE CHARGE CHARACTERISTICS

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#### Typical Electrical Characteristics

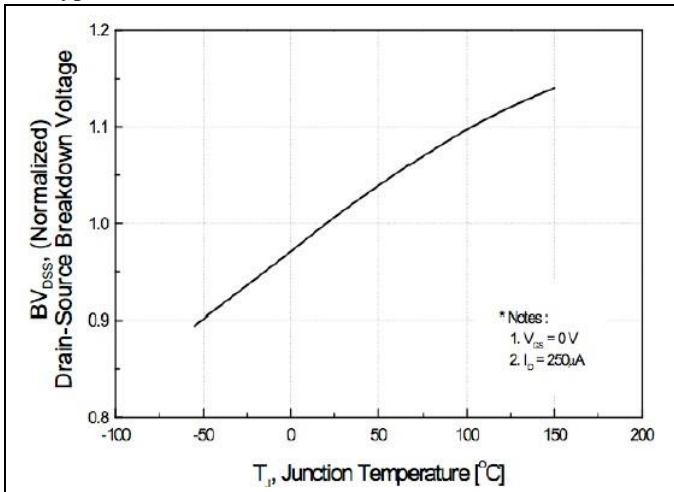


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

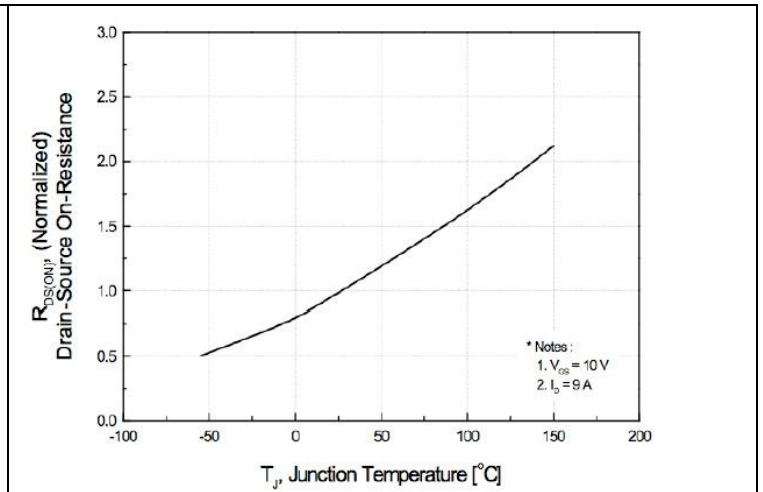


FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

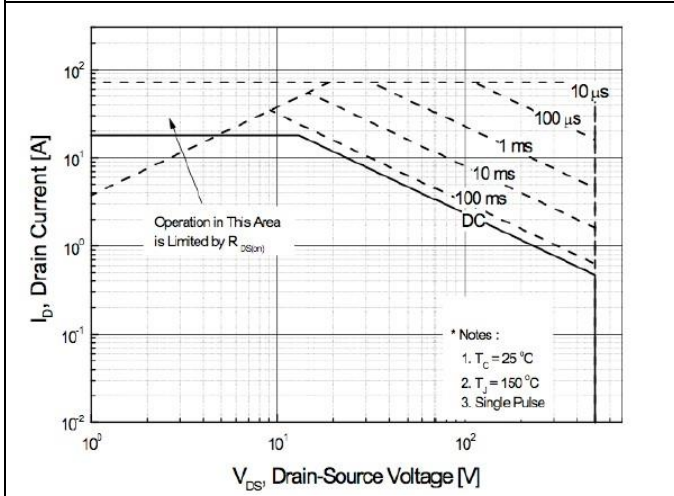


FIG.9-MAXIMUM SAFE OPERATING AREA

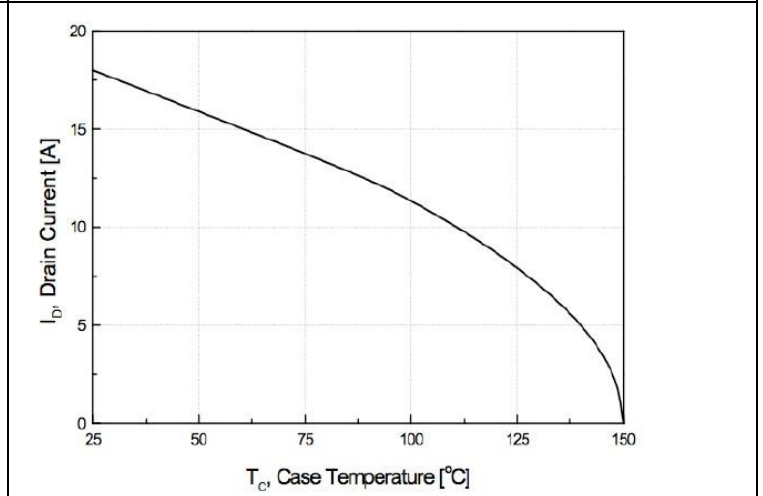


FIG.10-MAXIMUM DRAIN CURRENT VS CASE TEMPERATURE

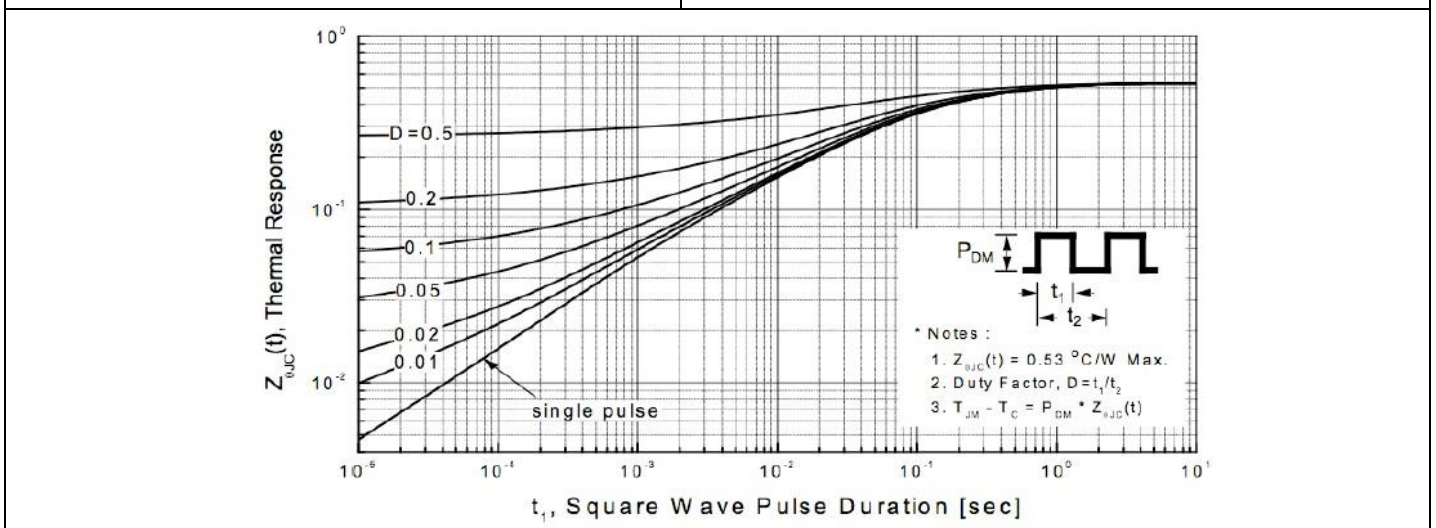


FIG.11-TRANSIENT THERMAL RESPONSE CURVE

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#### ■ Characteristics Test Circuit & Waveform

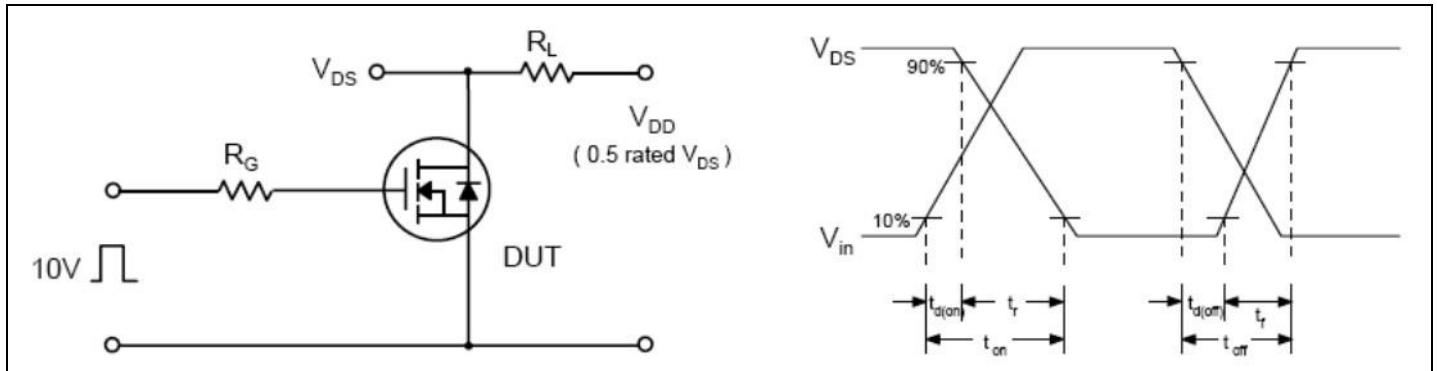


FIG.12-RESISTIVE SWITCHING TEST CIRCUIT & WAVEFORMS

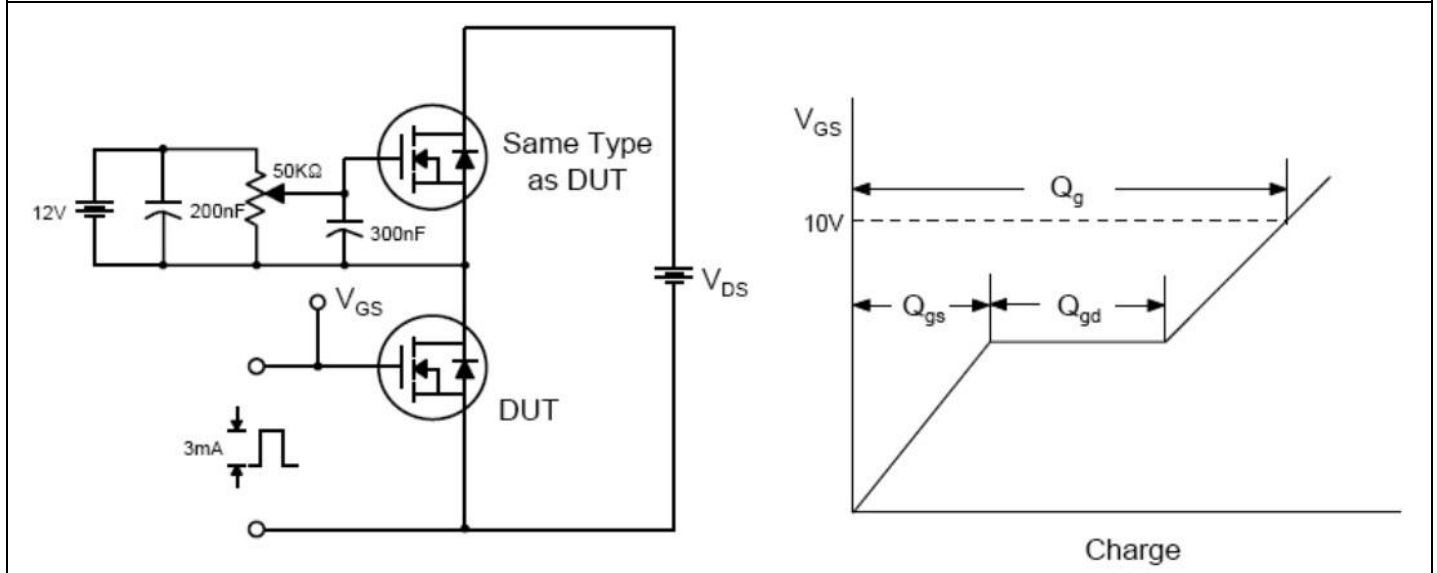


FIG.13-GATE CHARGE TEST CIRCUIT & WAVEFORM

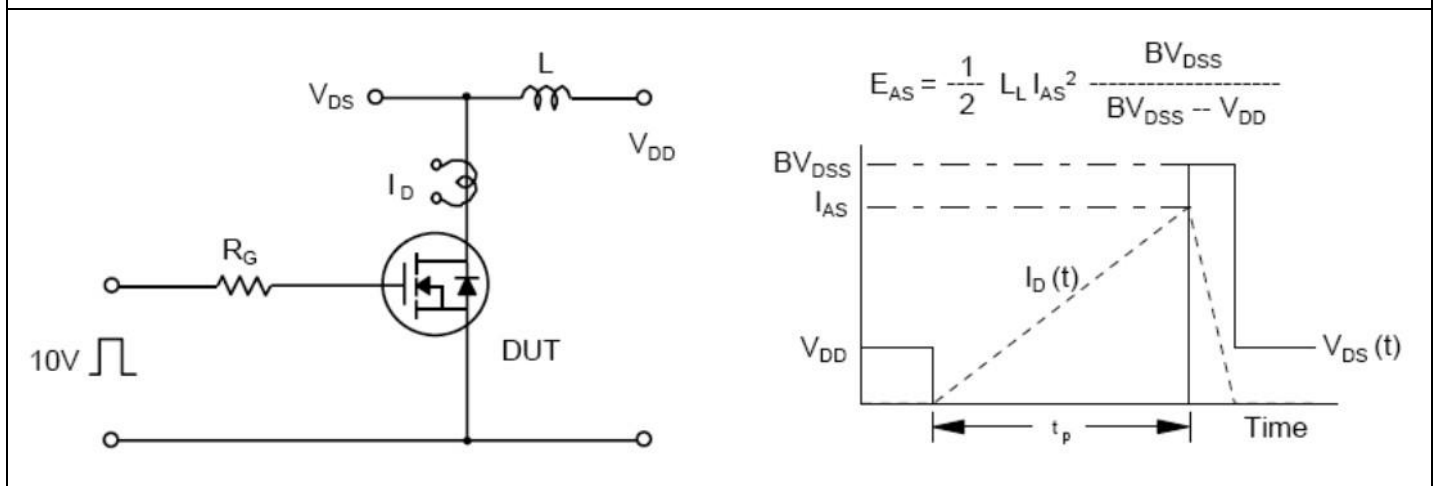


FIG.14-UNCLAMPED LINDUCTIVE SWITCHING TEST CIRCUIT & WAVEFORMS

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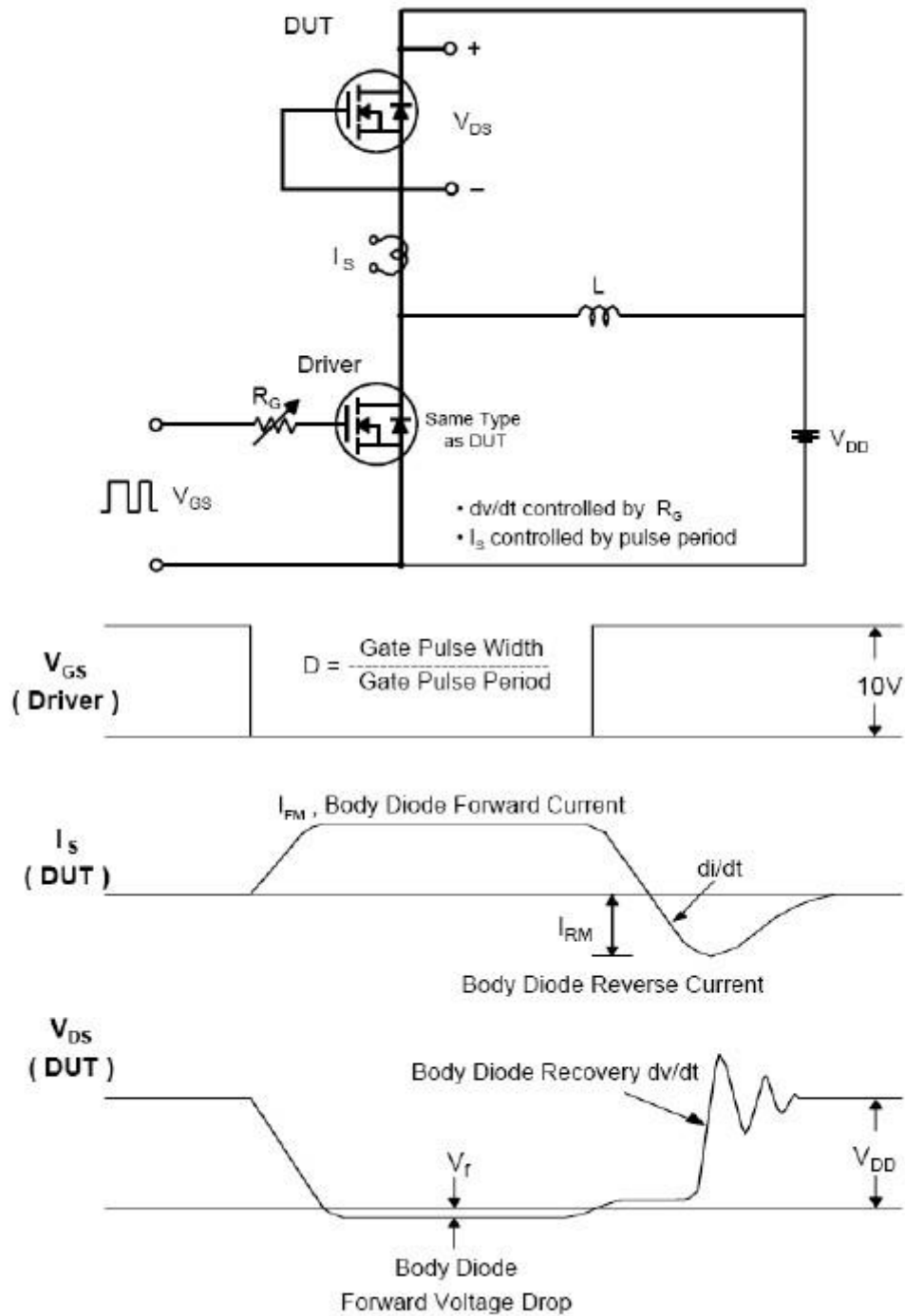


Fig 15. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms

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#### Disclaimer

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