

LOW CAPACITA NCE TVS ARRAY

Features

- ESD Protection for 1 Line with Bi-directional.
- Provide ESD protection for the protected line to
- IEC 61000-4-2 (ESD) ± 20 kV (air), ± 12 kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 7A (8/20µs)
- · Cable Discharge Event (CDE)
- Ultra-small SOD-323 package saves board space.
- Protect one I/O line or one power line
- · Fast turn-on and Low clamping voltage
- · Low operating voltage: 5V
- · Solid-state silicon-avalanche and active circuit

triggering technology

· RoHS Compliant Package

Application

- · Computer Interfaces Protection
- · Microprocessors Protection
- · Serial and Parallel Ports Protection
- Control Signal Lines Protection
- · Power lines on PCB Protection
- · Latchup Protection

Mecanical Data

- · Molded JEDEC SOD-323 Package
- · Approximate Weight: 5 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature: Pure-Tin Sn, 100:

260-270°C

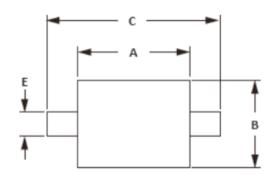
- 8mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

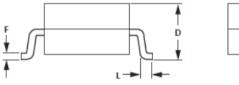
Packing &Order Information

3,000/Reel







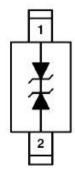


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	OUTLINE DIMENSIONS				
DIM	MILLIN	METERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
Α	1.60	1.90	0.063	0.075	
В	1.15	1.45	0.045	0.057	
С	2.39	2.70	0.094	0.106	
D	0.80	1.10	0.031	0.043	
Е	0.25	0.40	0.010	0.016	
F	0.10	0.20	0.004	0.008	
Н	-	0.10	-	0.004	
L	0.20	-	0.008	-	

- Controlling dimension: millimeters
- Dimensioning and tolerances per ANSI Y14.5M, 1985.
 Dimensions are exclusive of mold flash and metal burrs.

Graphic symbol



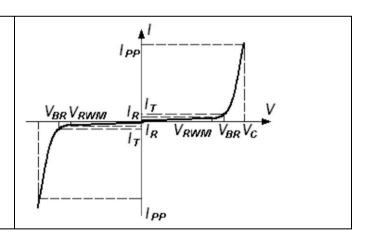


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Absolute r	atings (limiting value)			
Symbol	Parameter	Value	Unit	
PPK	Peak Pulse Power (tp = 8/20μs)	110	W	
IPP	Peak Pulse Current (tp = 8/20µs) 8.5 A			
VDC	Operating Supply Voltage (pin-1,-2 to pin-3)	6 V		
VCL	Clamping Voltage (IPP=5A, tp=8/20us, T=25 °C)	8	V	
VCL	Clamping Voltage (IPP=5A, tp=8/20us, T=25 °C)	9	V	
VESD	ESD per IEC 61000-4-2 (Air)	±22	KV	
VESD	ESD per IEC 61000-4-2 (Contact)	±15		
TJ	Operating Supply Voltage (pin-1,-2 to pin-3)	-55 to +125	°C	
TSTG	Storage Temperature Range	-55 to +150	°C	
TL	Lead Solder Temperature – Maximum (10 Second Duration)	ad Solder Temperature – Maximum (10 Second Duration) 260 °C		

Electrical Character	ris tic s						
	VBR		ΙΤ	VRWM	ID	Cj	
Part Numbers	Min.	Тур.	Max.	11	V K W W	IR .	TYP
	V	V	V	m A	V	u A	PF
ESDV05CL	6.1	7	9	1	5	25	15

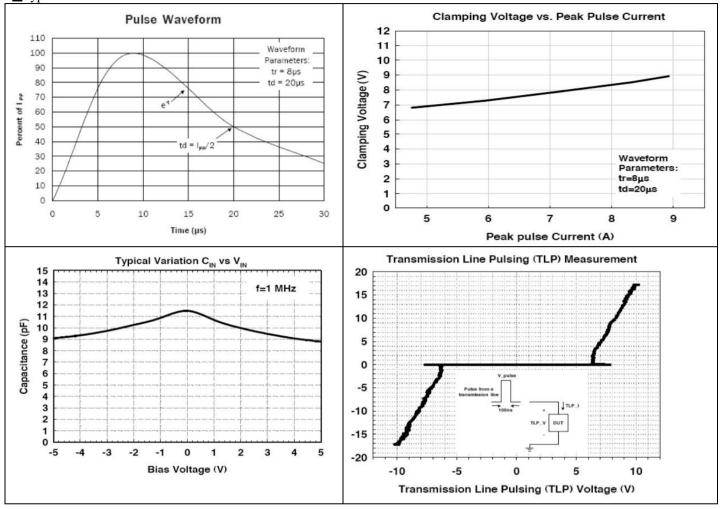
Symbol	Parameter			
I _{PP}	Maximum Reverse Peak Pulse Current			
Vc	Clamping Voltage @ I _{PP}			
V_{RWM}	Working Peak Reverse Voltage			
I _R	Maximum Reverse Leakage Current @ V _{RWM}			
I _T	Test Current			
V_{BR}	Breakdown Voltage @ I _T			





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■Typical Device Characteristics





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